

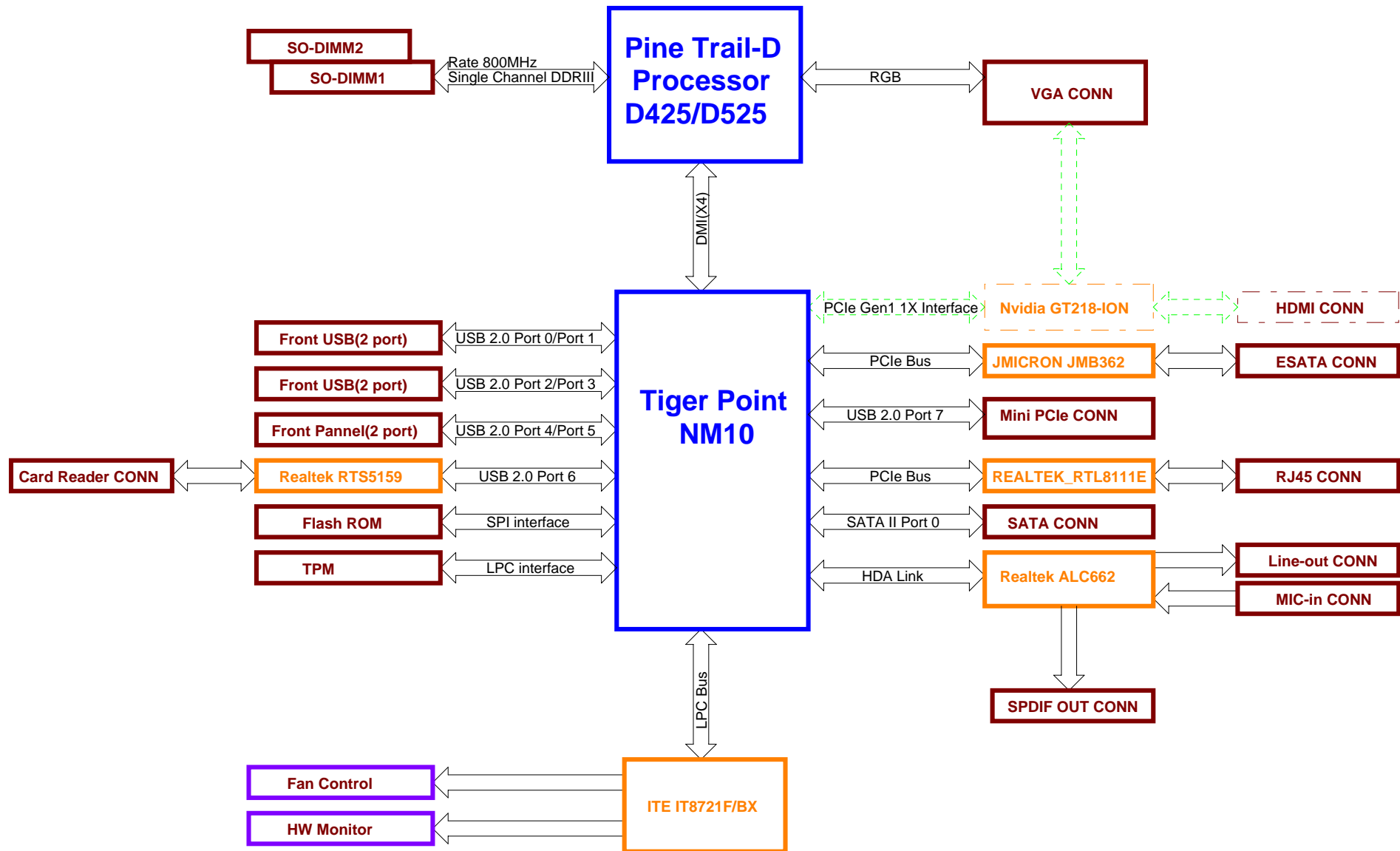
Foxconn Precision Co. Inc.

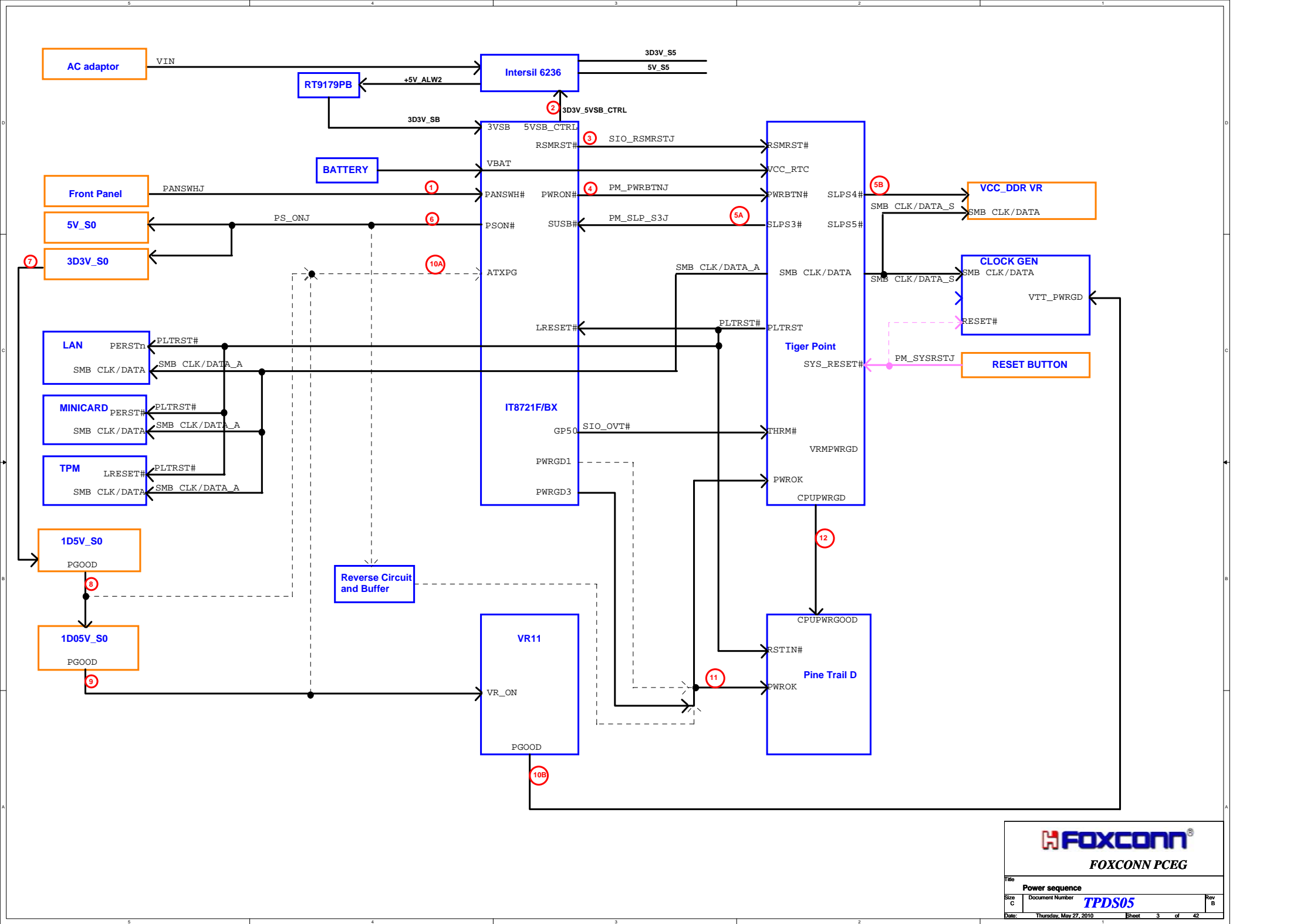
PINEVIEW-D Schematic

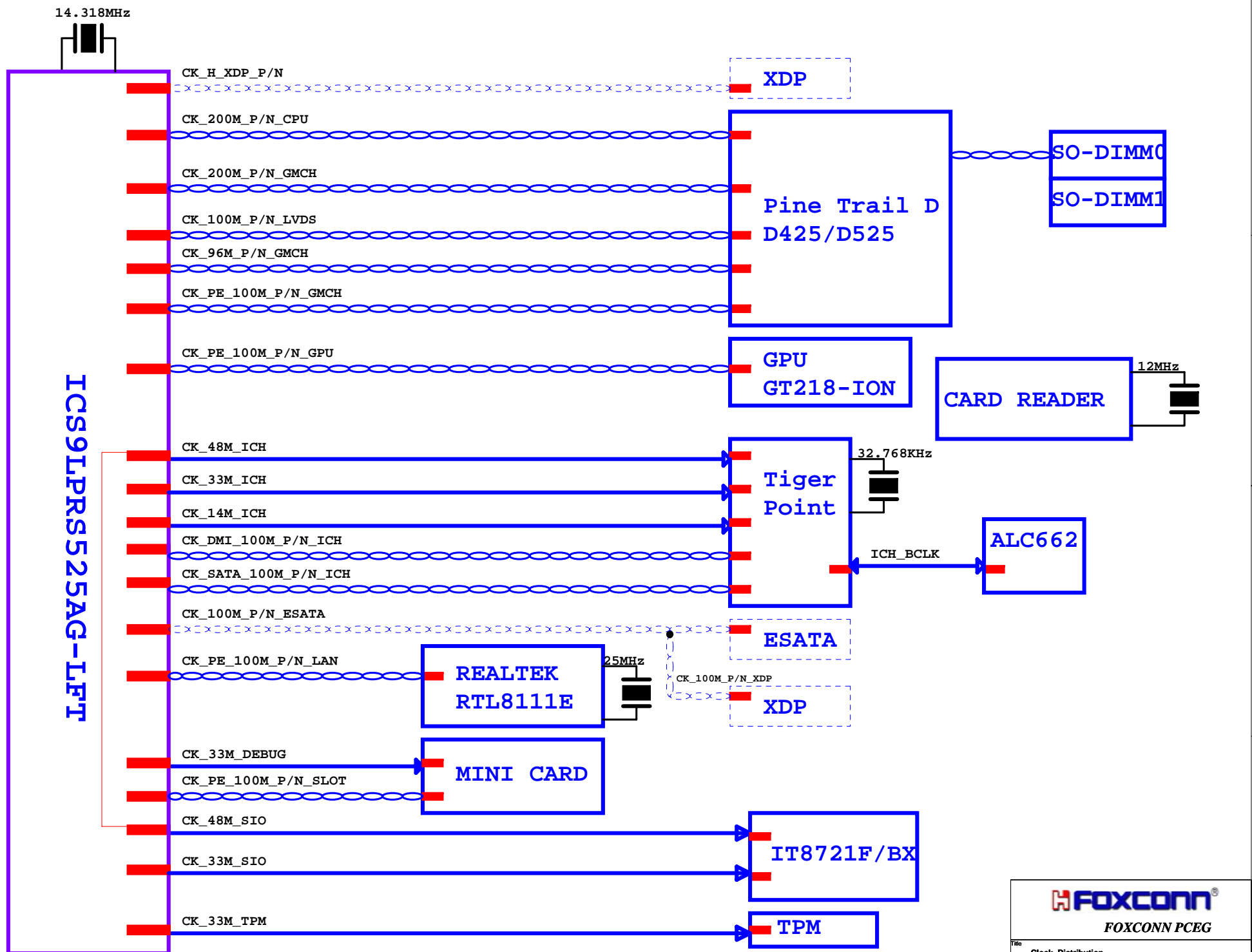
Fab B
Data: 2010/05/27

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Pineview-D(D425/D525)

D425: Single Core CPU	
D525: Dual Core CPU	
VCC_CORE_CPU	9640mA
VCCP	?mA
VCCA	80mA
VCC_DDR	2270mA
VCCCK_DDR	
VCCSFR_DMIHPLL	104mA
VCCSFR_AB_DPL	154mA
VCCACRTDAC	
VCCA_LVDS	80mA
VCCD_LVDS	
VCCA_DMI	480mA
VCCRING_EAST	
VCCRING_WEST	
VCC_LGI	305mA
VCCD_AB_DPL	
VCCD_HMPLL	
VCCA_DDR	1320mA
VCCACK_DDR	
VCC_GFX	3360mA
VCC_GIO	5mA

Nvidia GT218-ION(DDR3)

Power Name	Consumption
FB_PLLAVDD	100mA/
FB_DLLAVDD	100mA/
IFPAB_PLLVDD	220mA/
IFPC_IOVDD	285mA/
IFPD_IOVDD	285mA/
IFPE_IOVDD	285mA/
PEX_IOVDD/Q	530mA/1.05V
PEX_PLLVDD	84mA/1.05V
PLLVD	60mA/
SP_PLLVDD	45mA/
VID_PLLVDD	45mA/
FBVDD	660mA/1.5V
FBVDDQ (GPU+MEM)	1300mA/1.5V
IFPA_IOVDD	150mA/
IFPB_IOVDD	150mA/
IFPC_PLLVDD	220mA/
IFPD_PLLVDD	220mA/
IFPE_PLLVDD	220mA/
DACA_VDD	120mA/
DACB_VDD	120mA/
PEX_SVDD_3V3	120mA/
VDD33	120mA/
NVVD	11.32A/0.9V

ISL6314CRZ(VR11)

VCC_CORE_CPU

Max 9640mA

1D5V_S0

Max 4041mA

ISL6268CAZ-T

VCC_DDR

Max 11.809A

ISL6268CAZ-T

1D05V_S0

Max 9979mA

RT9199PSP

1D8V_PLLSFR

Max 500mA

5V_S0

5V_S5

Max 3954mA

Intersil 6236

3D3V_S5

Max 5650mA

3D3V_S0

Max 2354mA

ON NCP5212A

NVVD

Max 1132mA

Single Channel DDRIII

0.75V VTT_DDR	500mA
1.5V VCC_DDR	2A*2

Tiger Point(NM10)

VCC1_5	1422mA
VCCUSBPLL	10mA
VCCDMIPLL	24mA
VCCSATAPLL	45mA
VCC1_05	955mA
V_CPU_IO	14mA
V5REF_SUS	10mA
V5REF	6mA
VCCSUS_3	92mA
VCC3_3	216mA
VCCRTC	6mA(G3)

Clock Generator

3D3V_S0	270mA
---------	-------

USB *6

5V_S3	500mA
-------	-------

MINI CARD

3D3V_S5	2750mA
1D5V_S0	500mA

SATA II

5V_S0	900mA
-------	-------

CARD READER

3D3V_S5	400mA
3D3V_S0	400mA

REALTEK ALC662

3D3V_S0	23mA
+5VA	38mA
	0.4mA(S3)

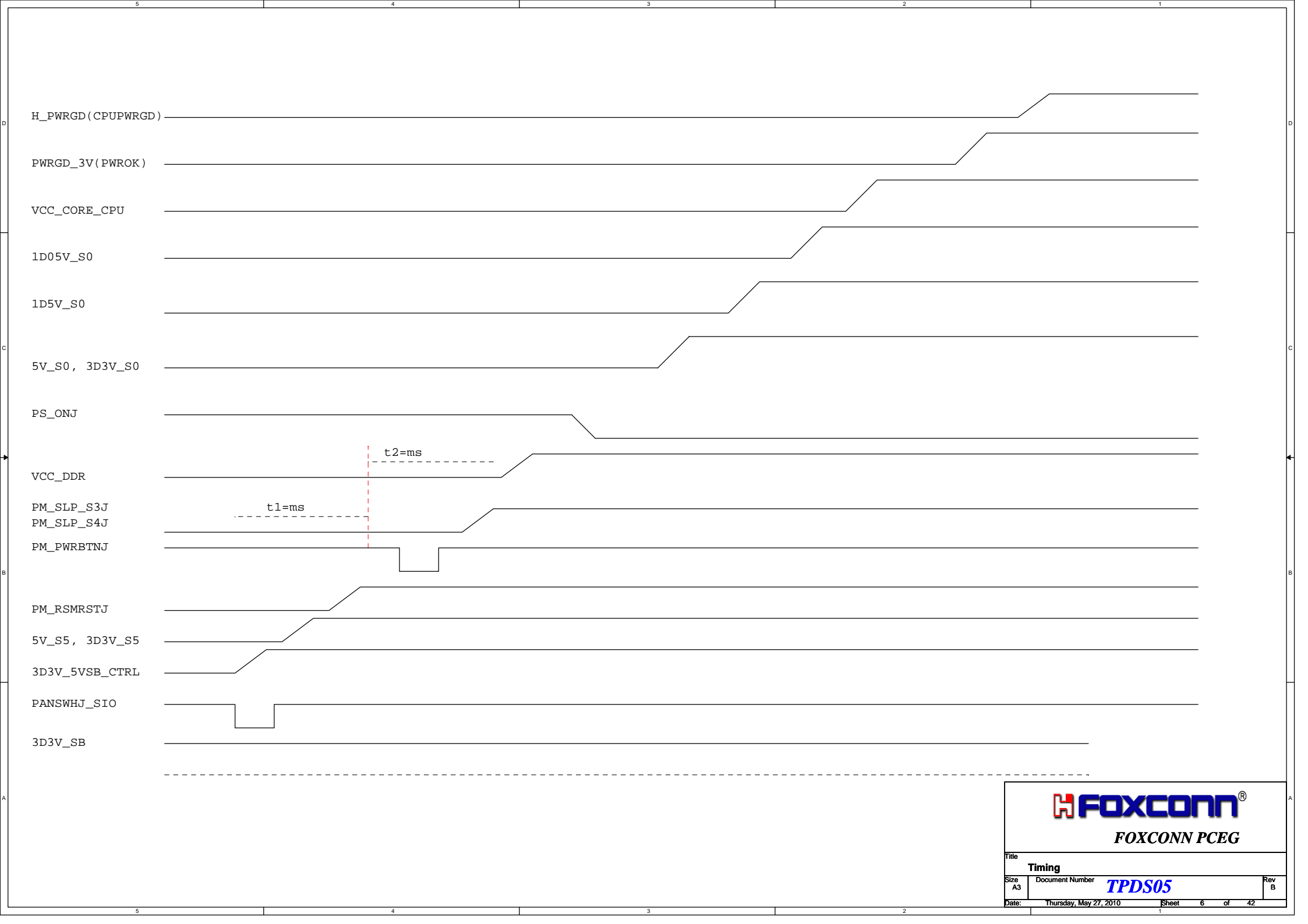
REALTEK RTL8111E

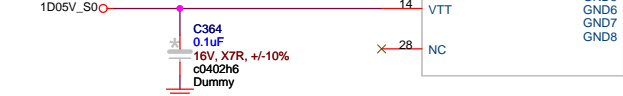
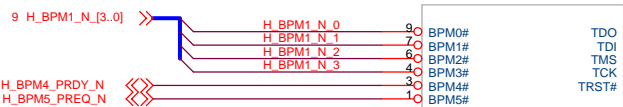
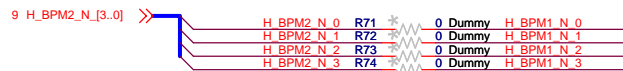
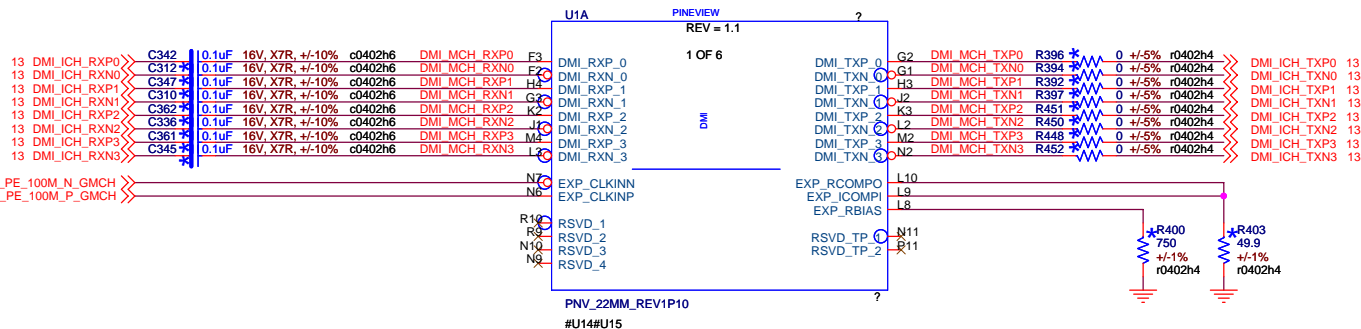
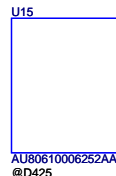
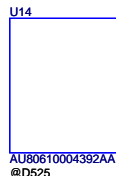
VDDO	69mA
AVDDL	151mA
VDDC	379mA



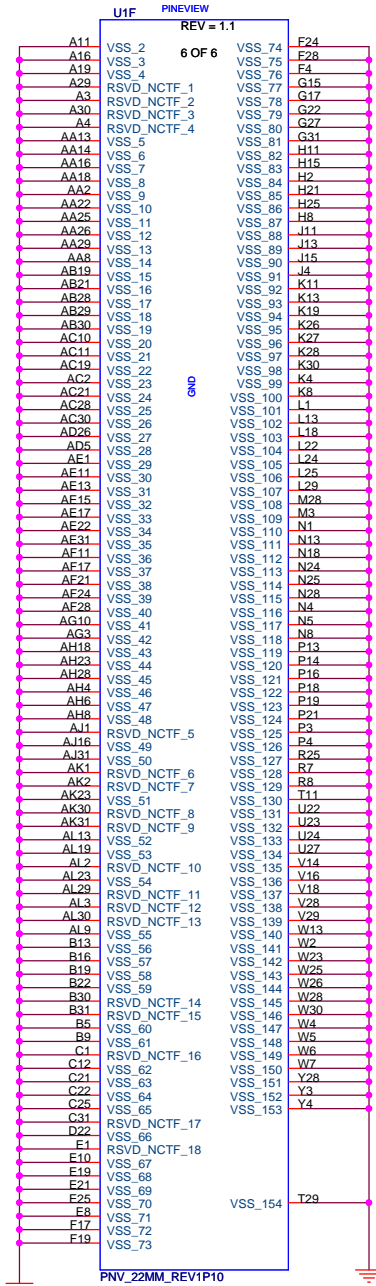
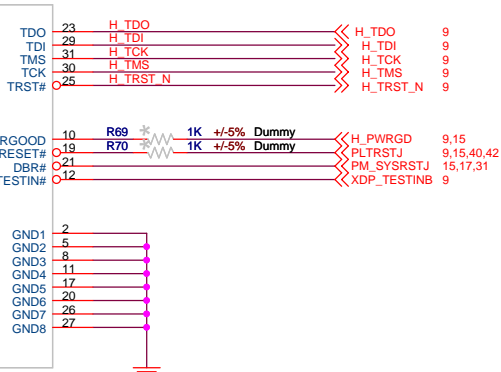
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Power Delivery	
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XDP_Header



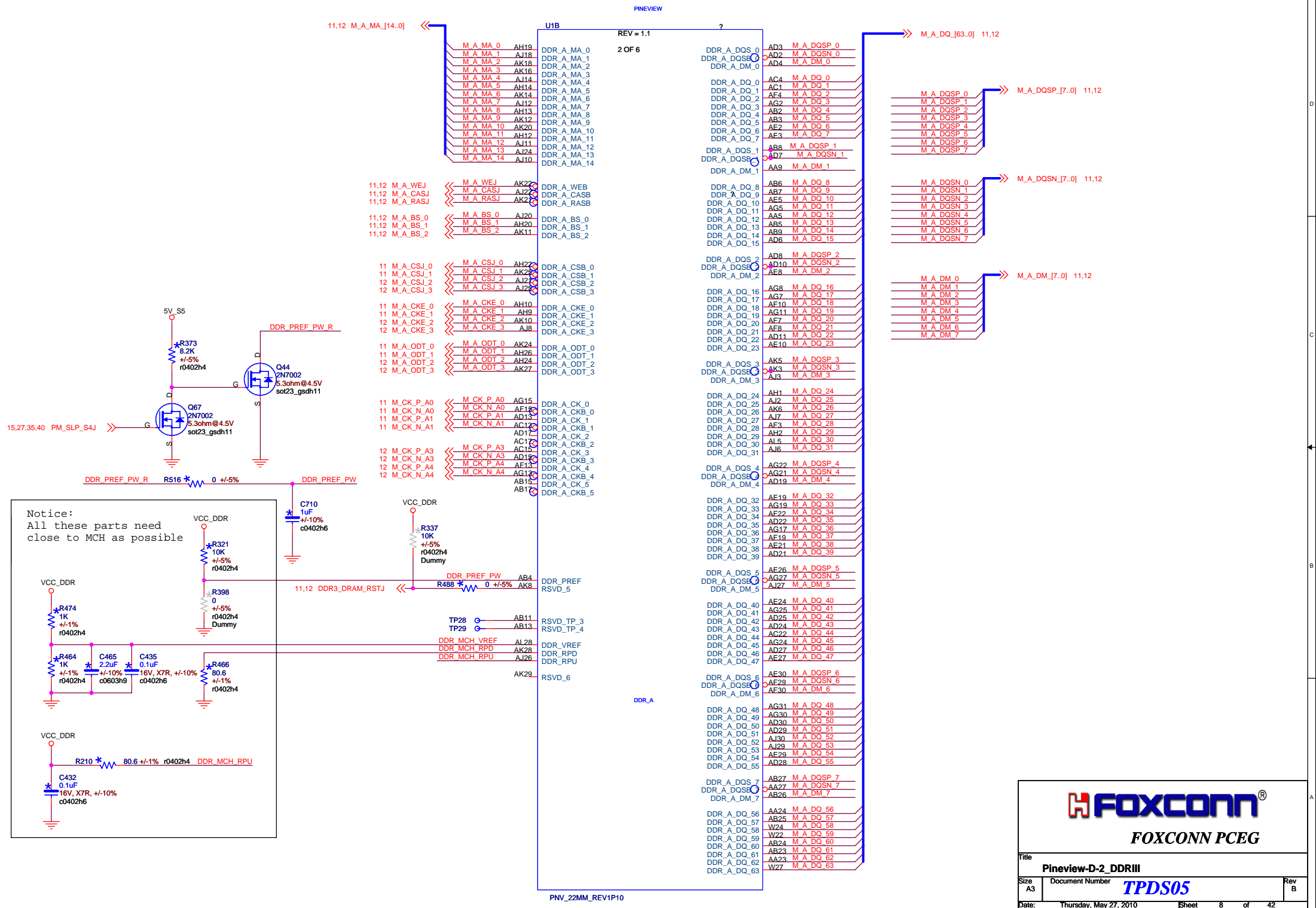
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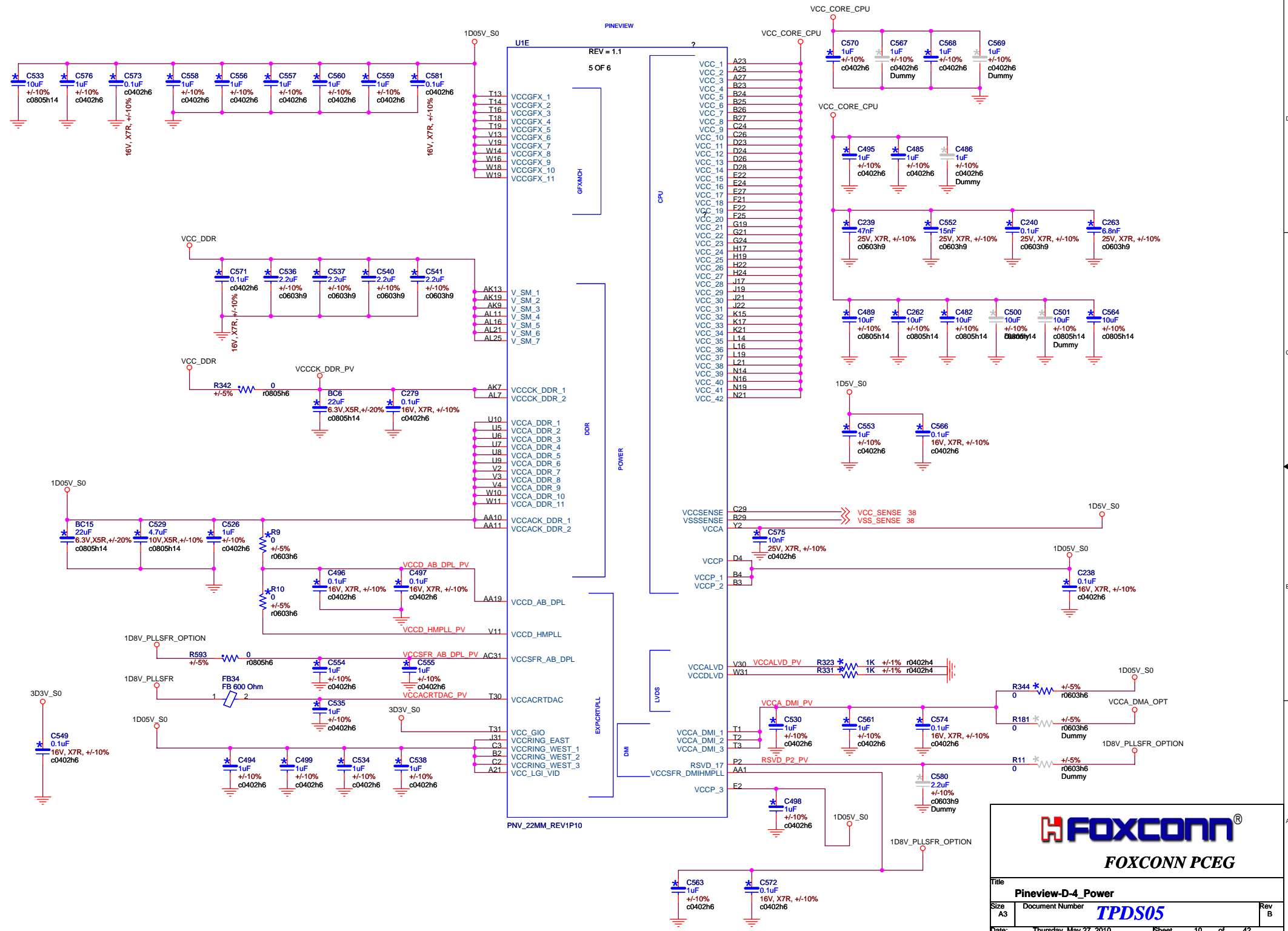
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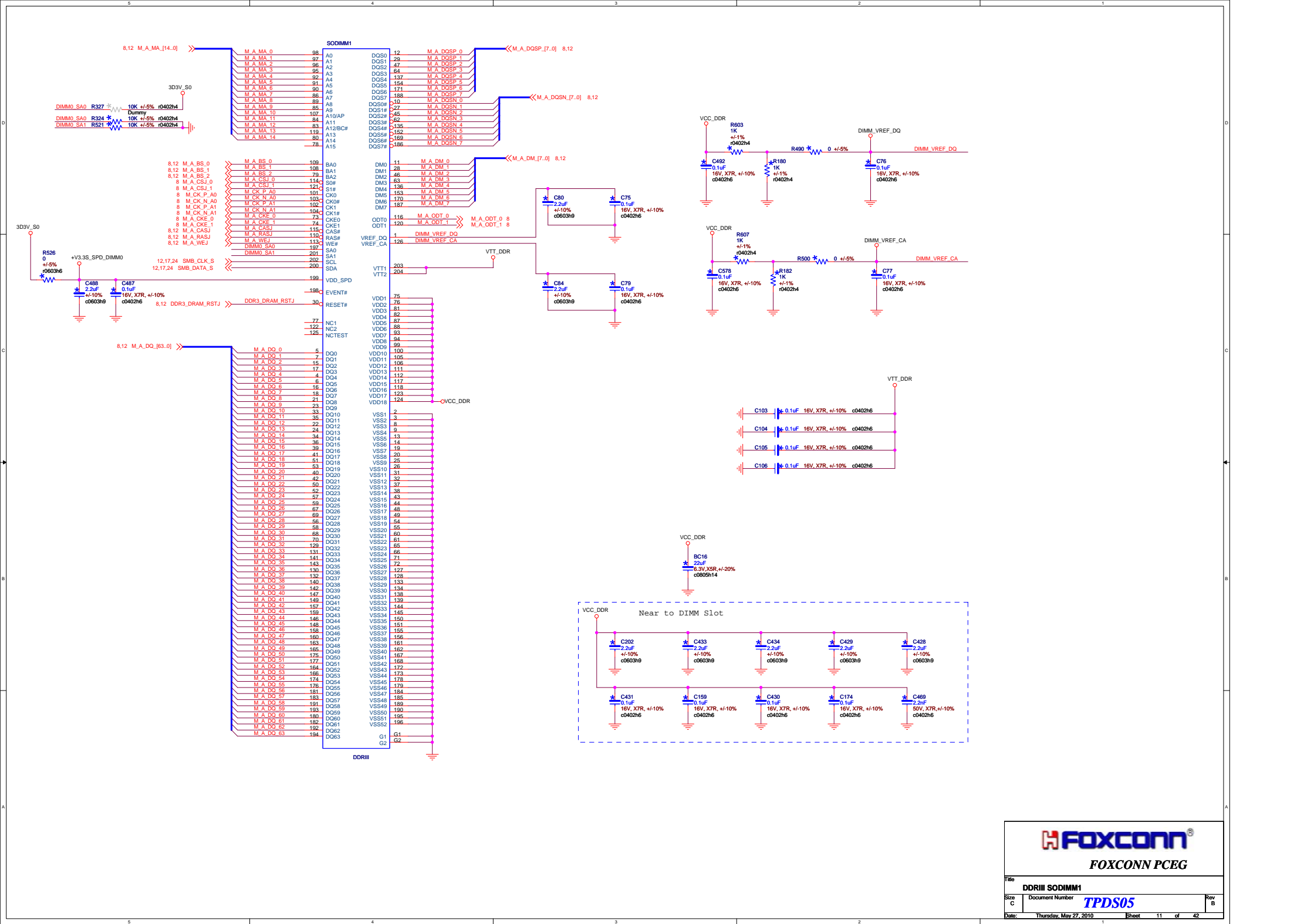
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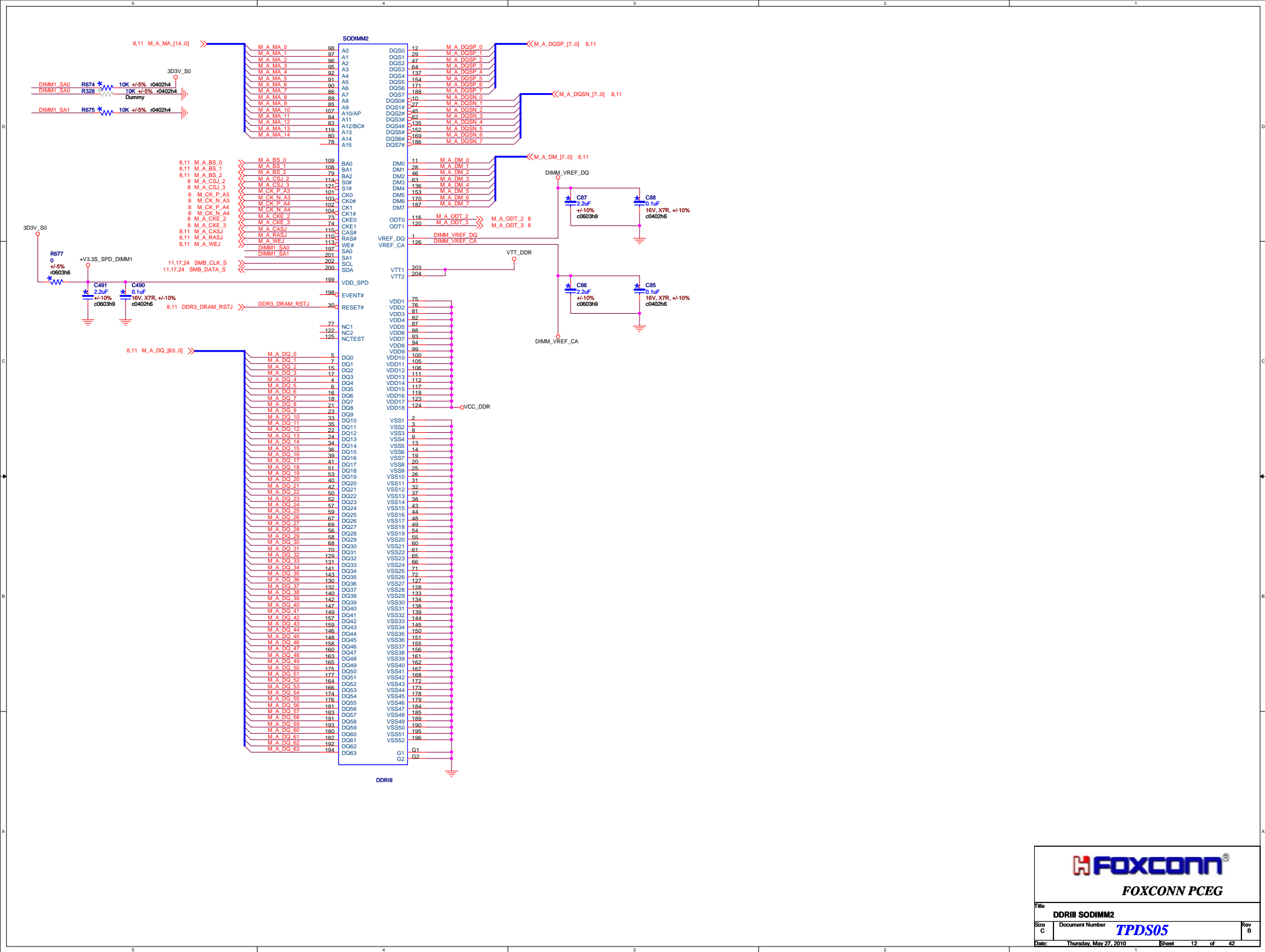
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Rev B





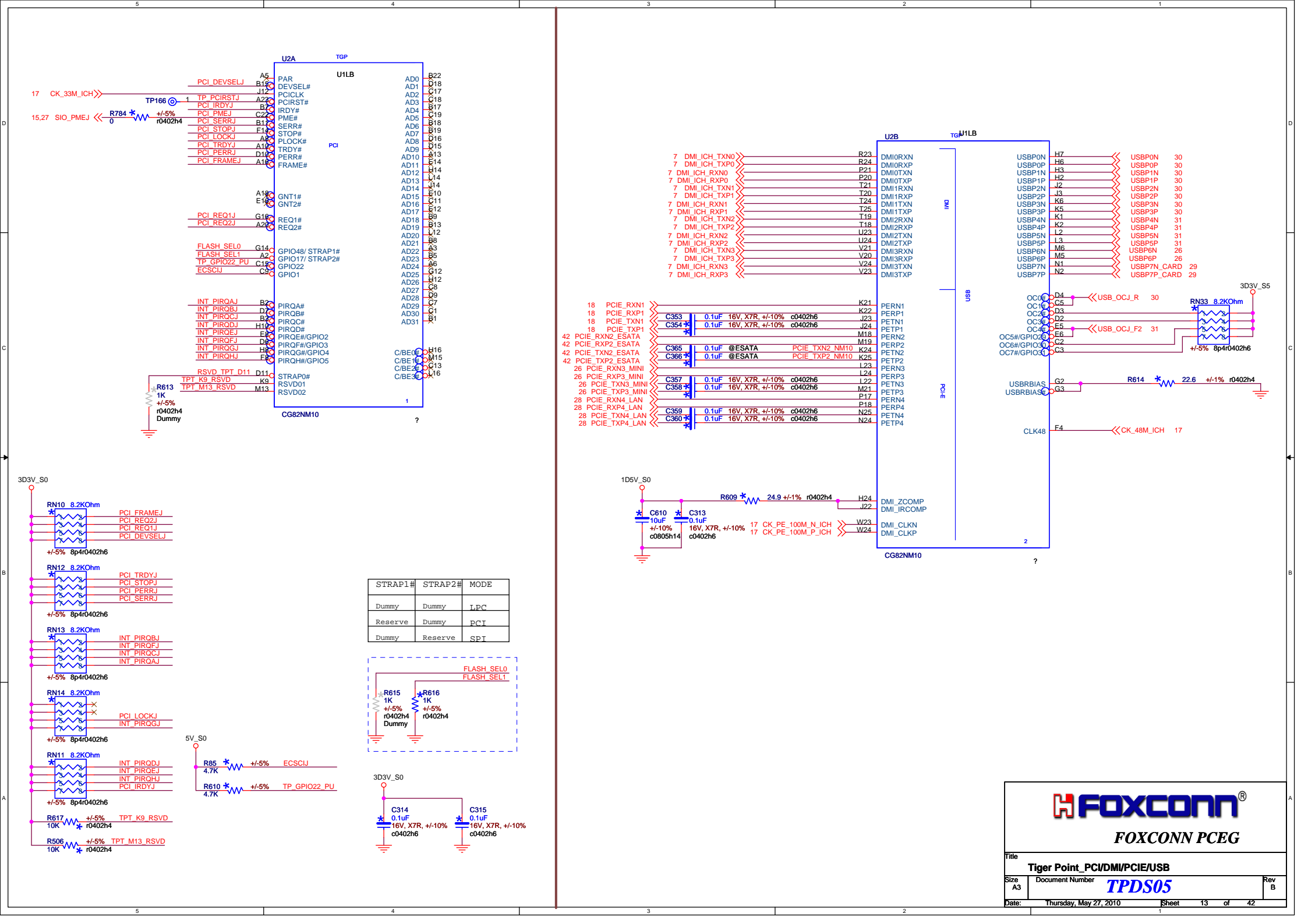


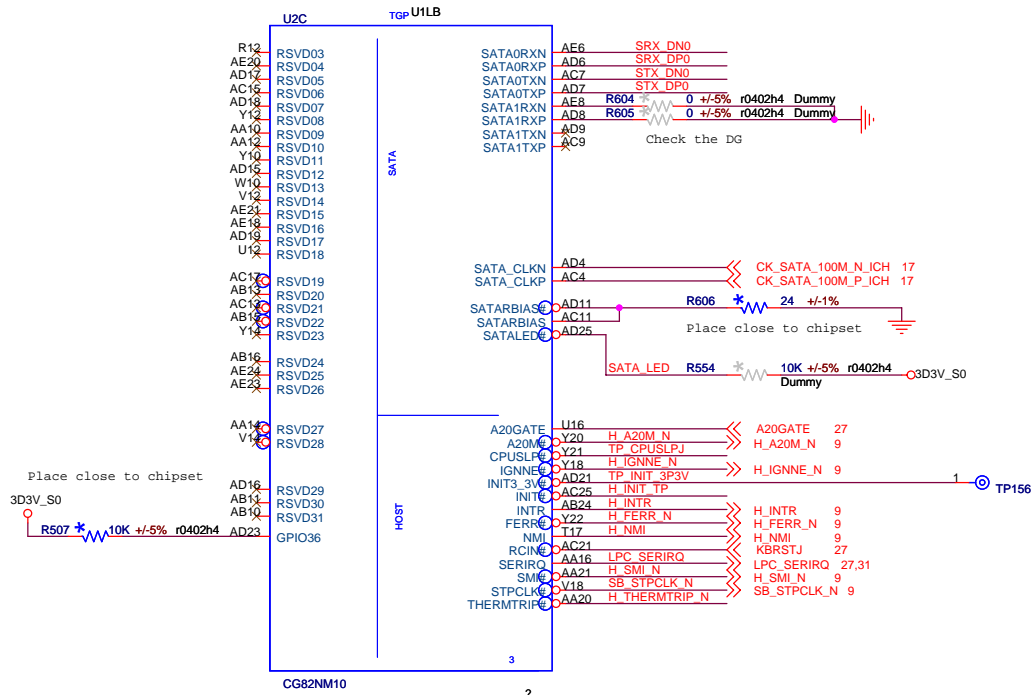
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DDRIII SODIMM2

Size C	Document Number	TPDS05
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Rev
B

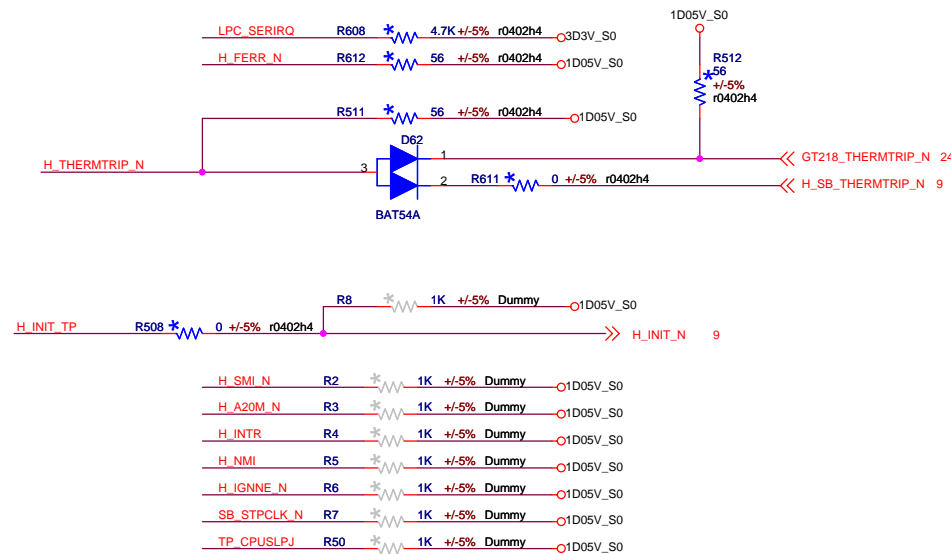
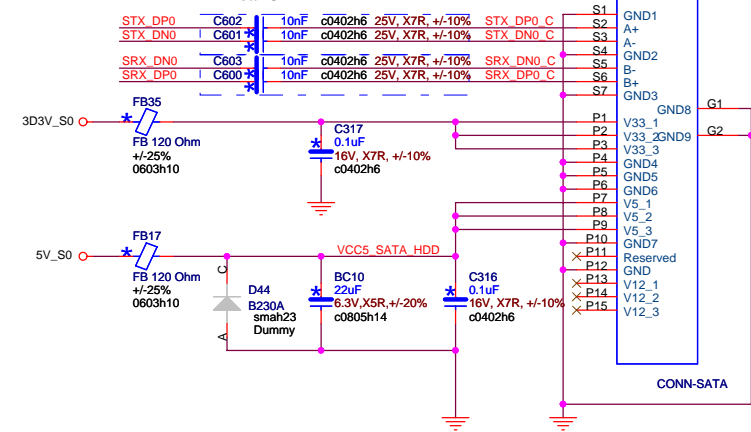
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SATA HDD CONNECTOR

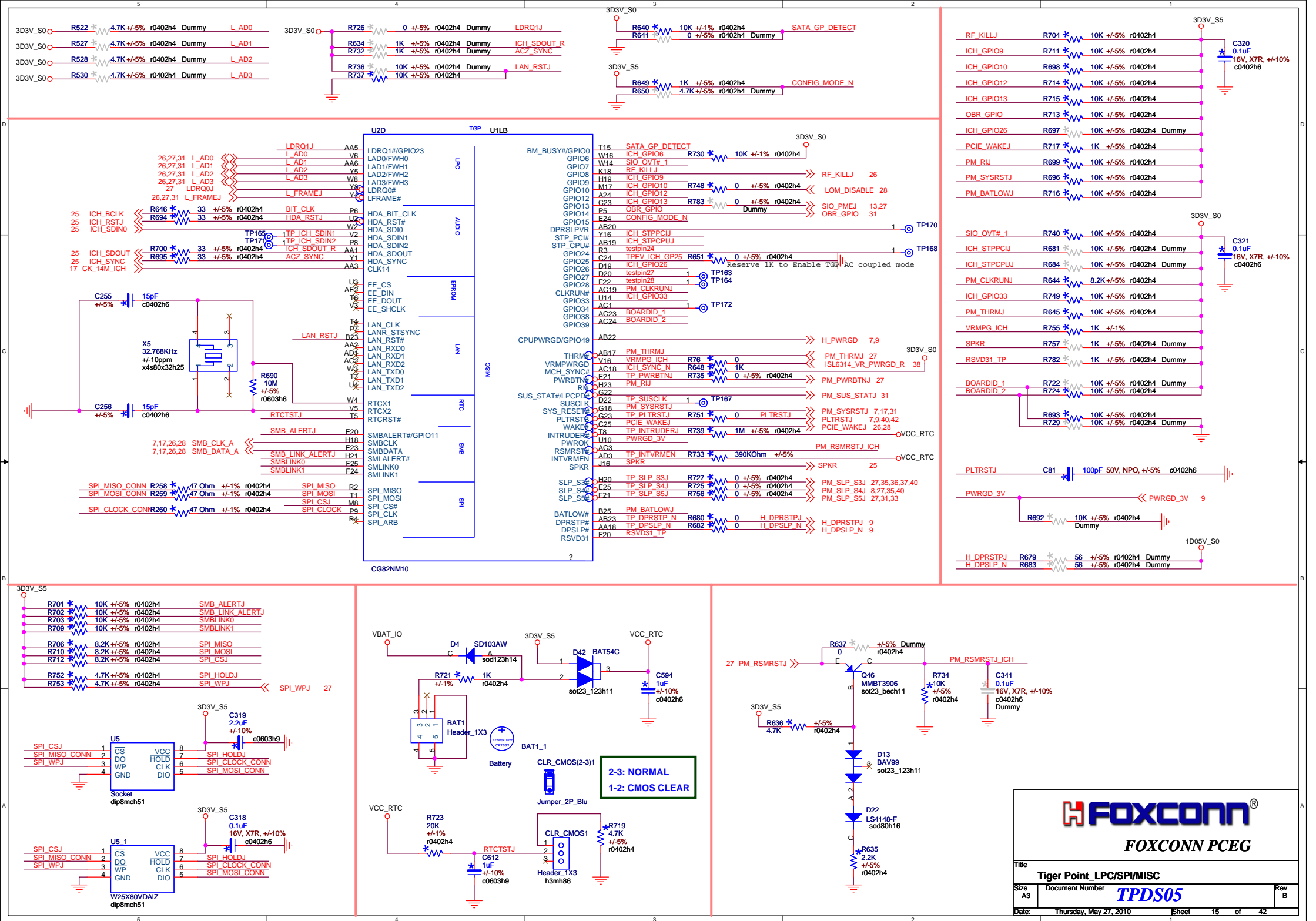
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near SB

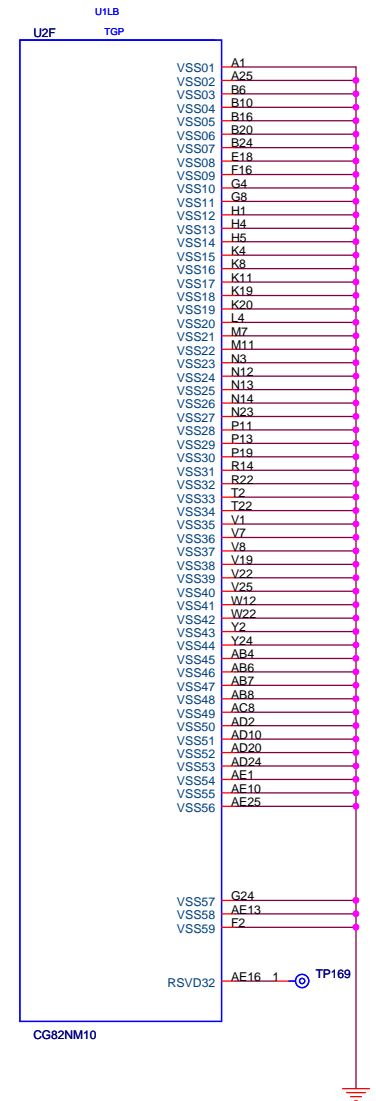
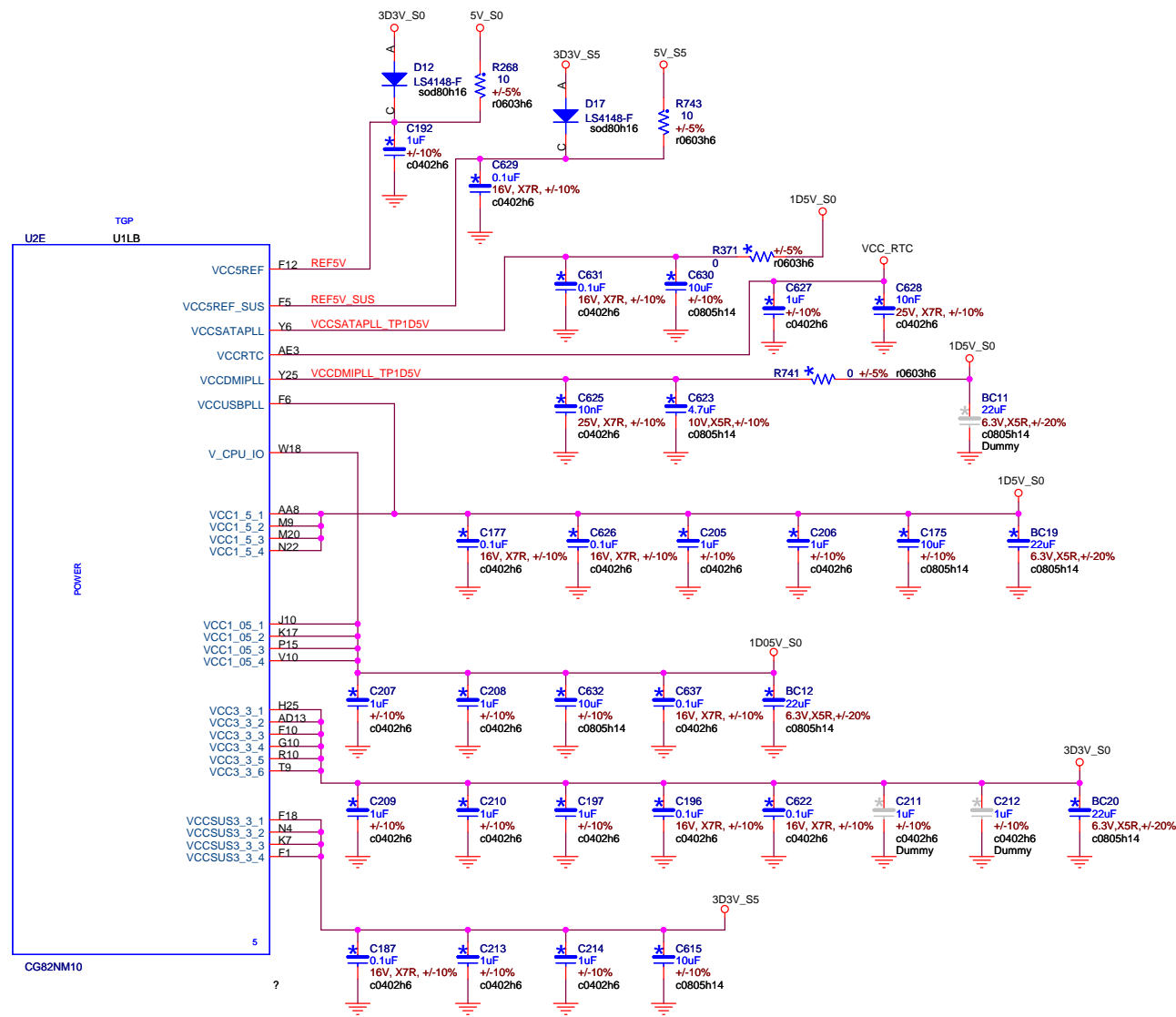


FOXCONN

FOXCONN PCEG

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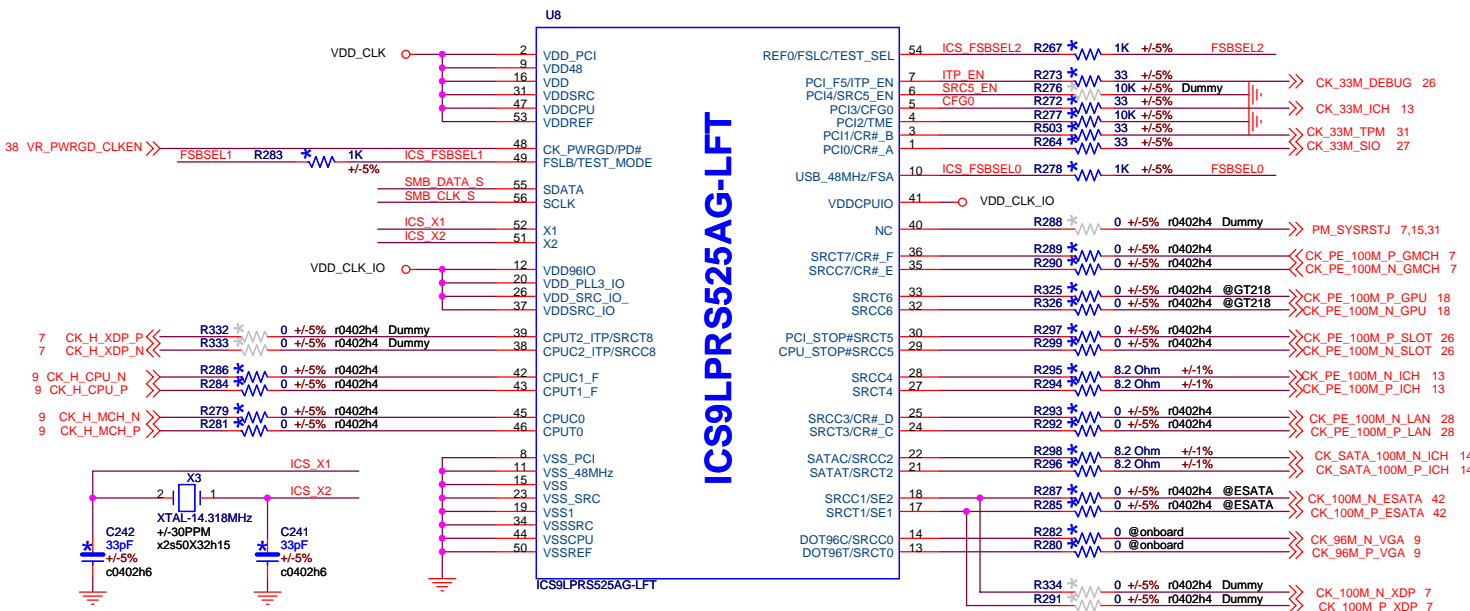
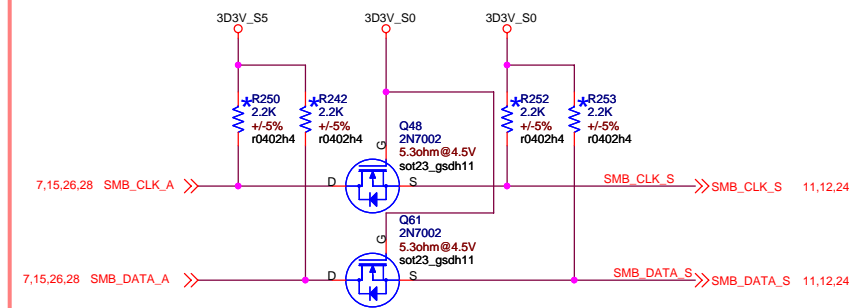
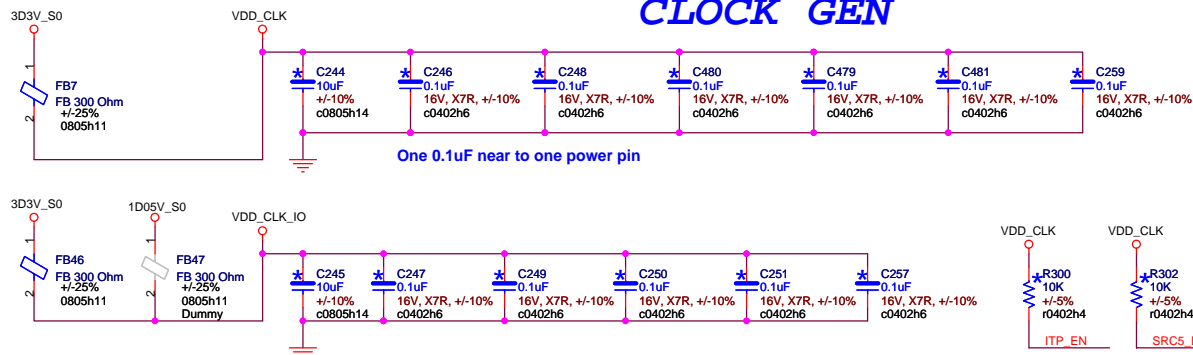




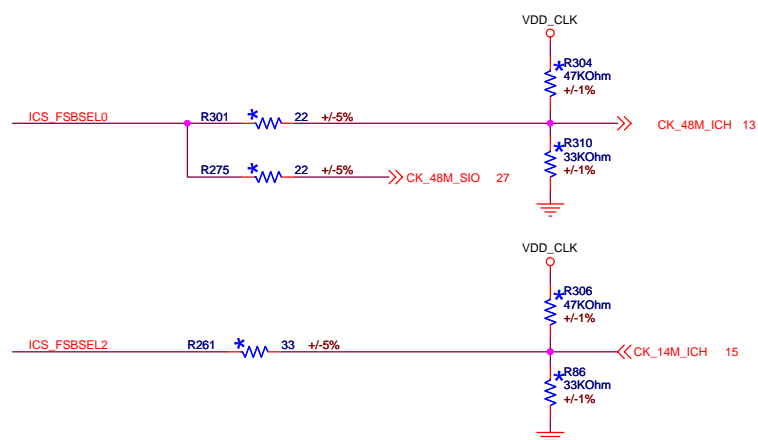
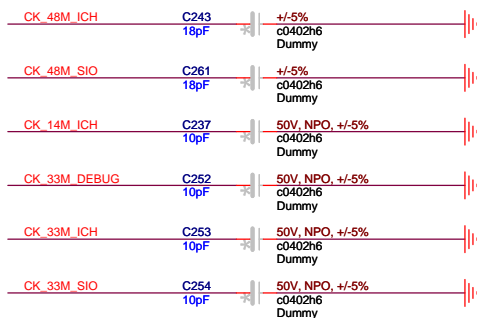
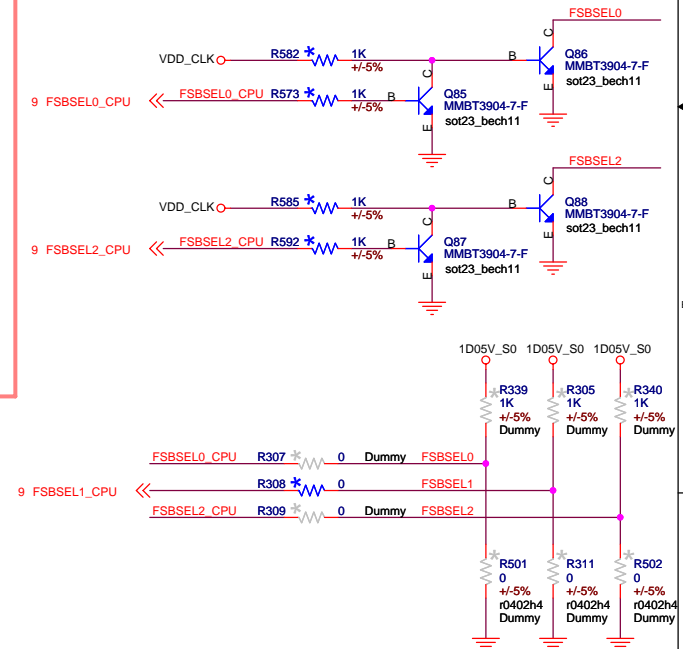
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Title		Tiger Point_Power/GND	
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CLOCK GEN

CPU,MCH and XDP BCLK FREQUENCY
SELECTION TABLE

FSC BSEL2	FSB BSEL1	FSA BSEL0	Host Clock frequency MHz
0	0	1	133
0	1	1	166
0	1	0	200

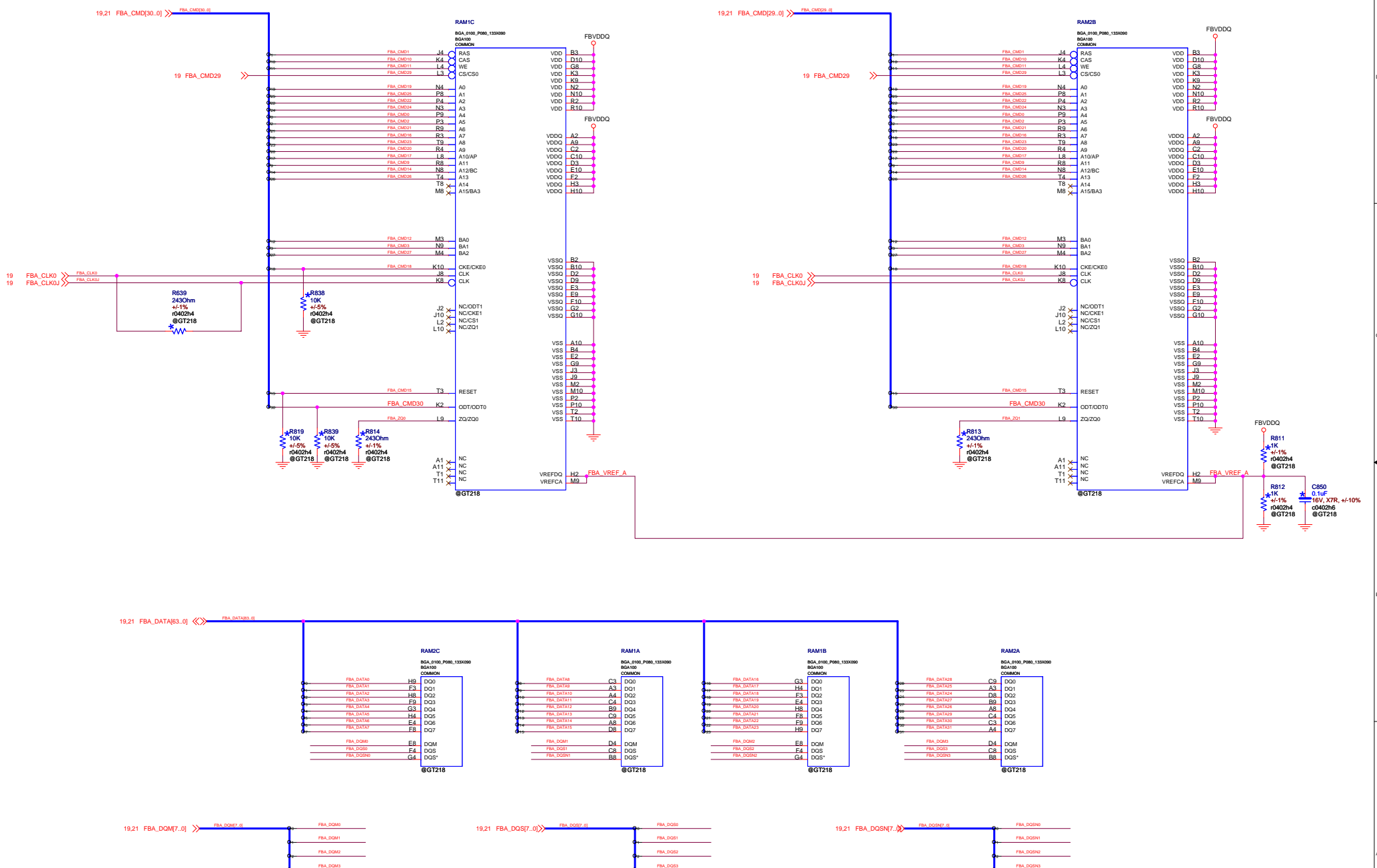


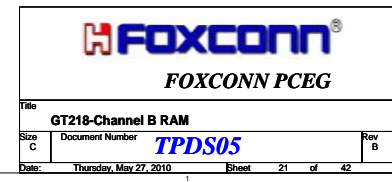
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Title **CLOCK GEN**

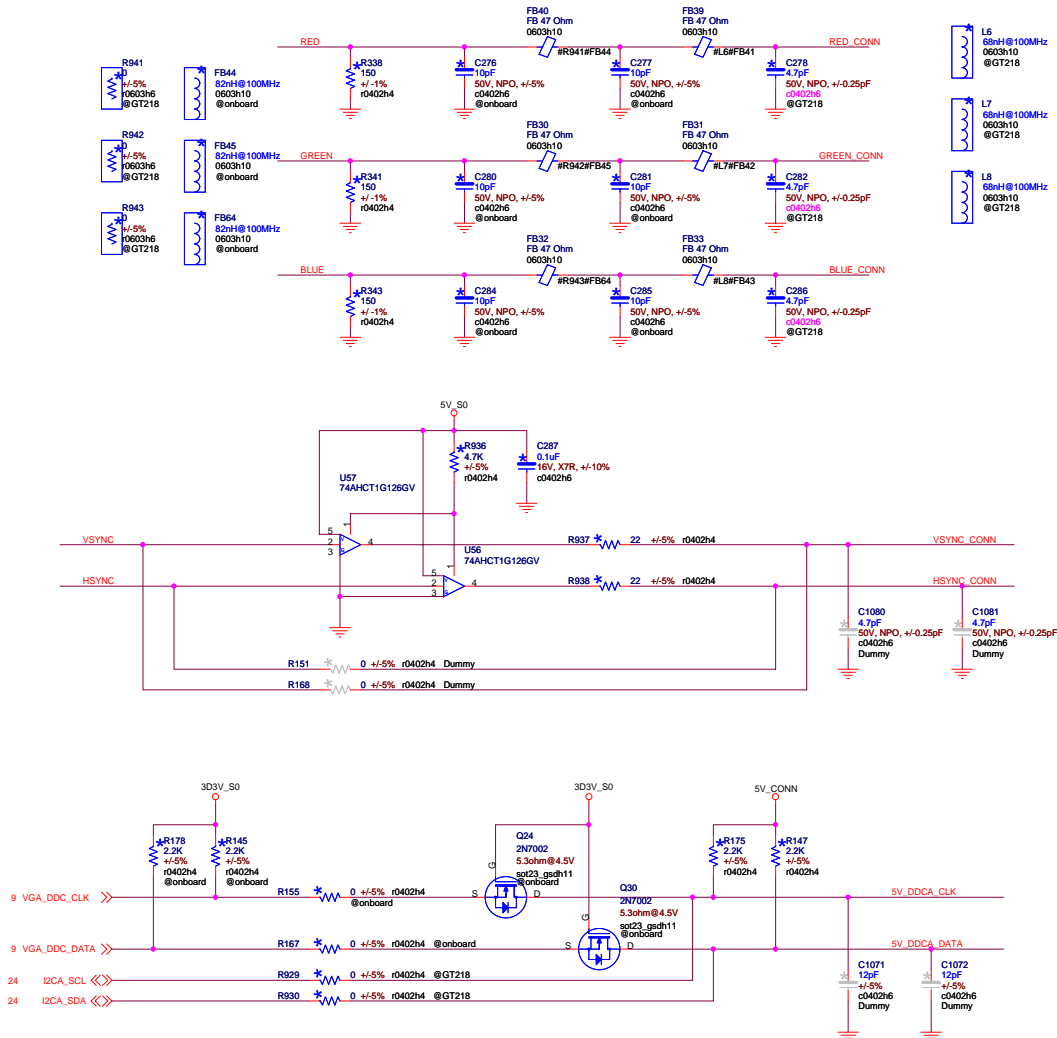
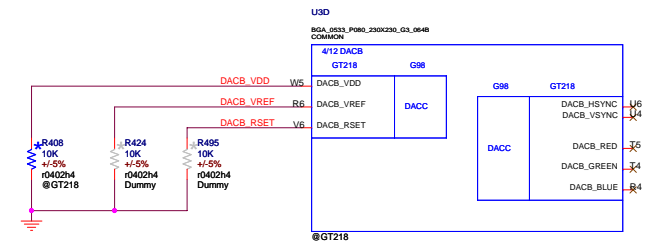
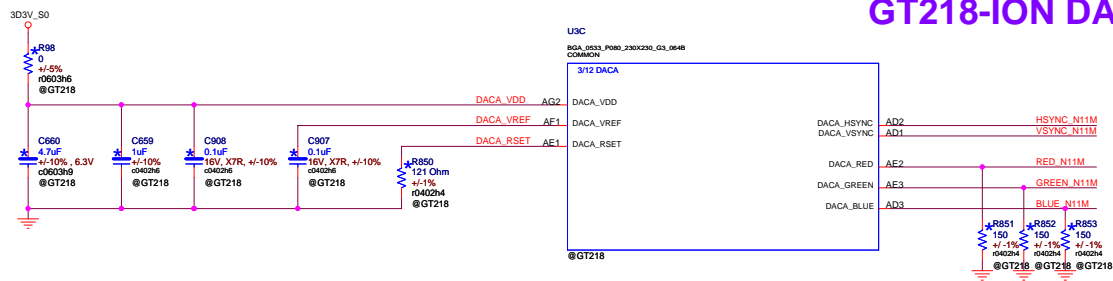
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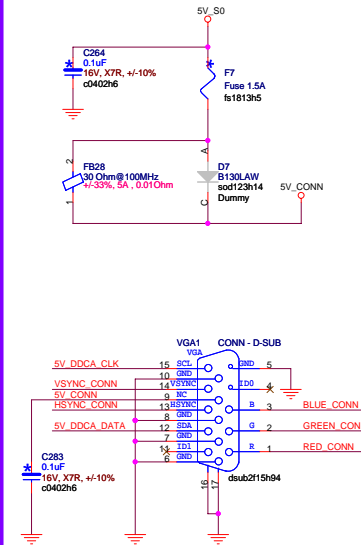




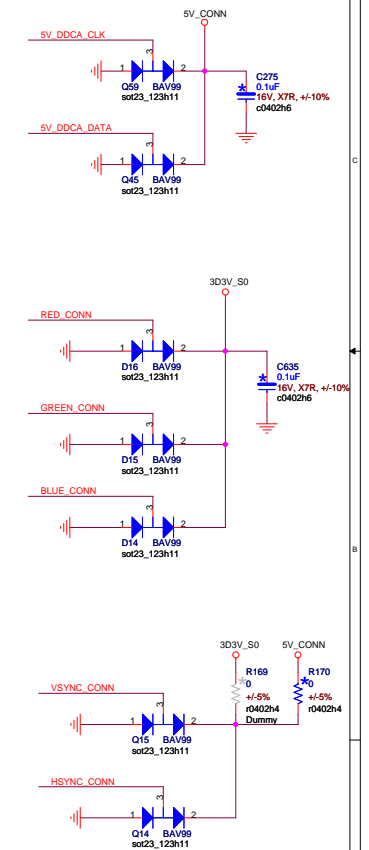
GT218-ION DAC Interface



VGA CONN



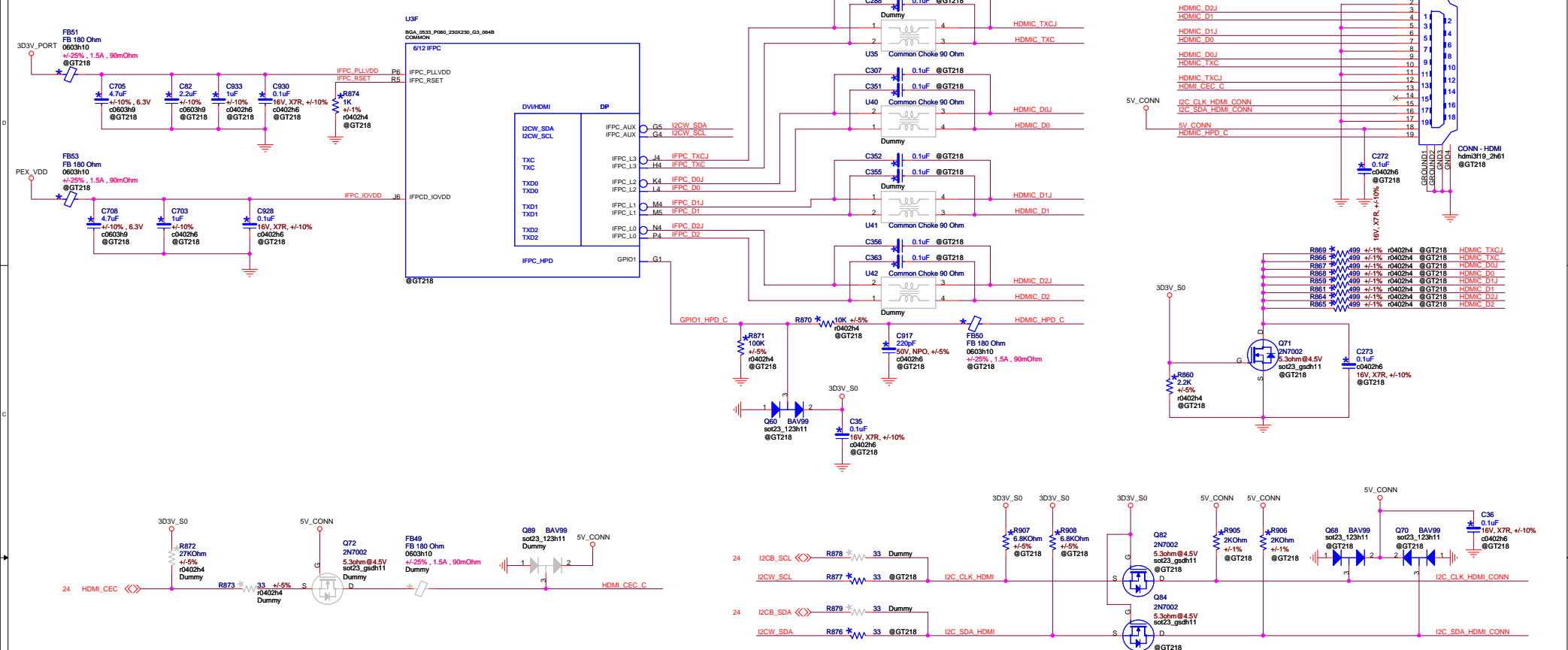
VGA ESD Schematic



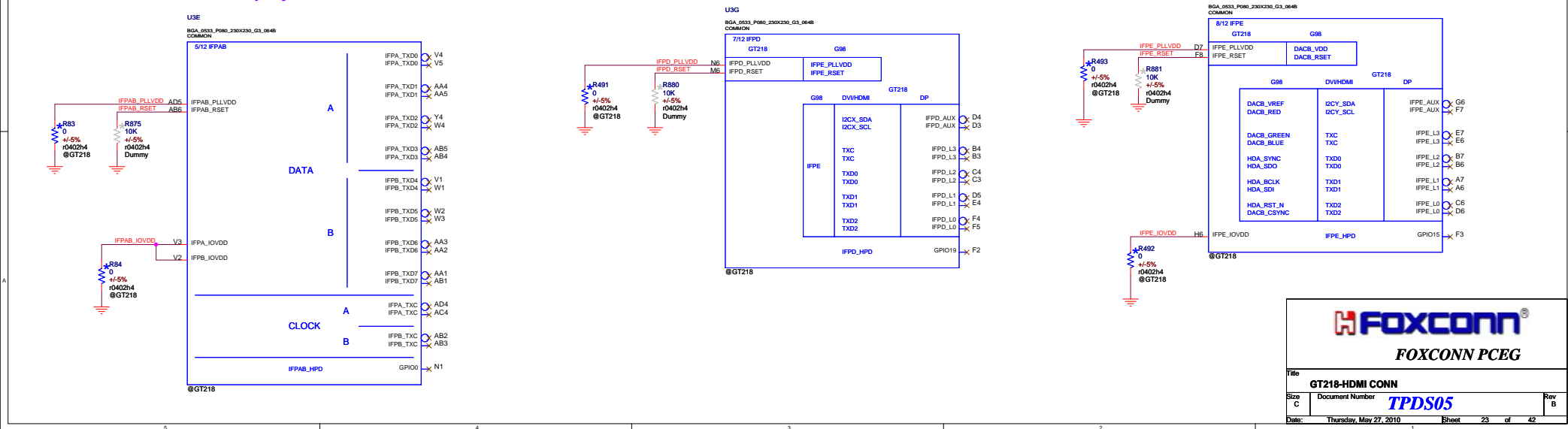
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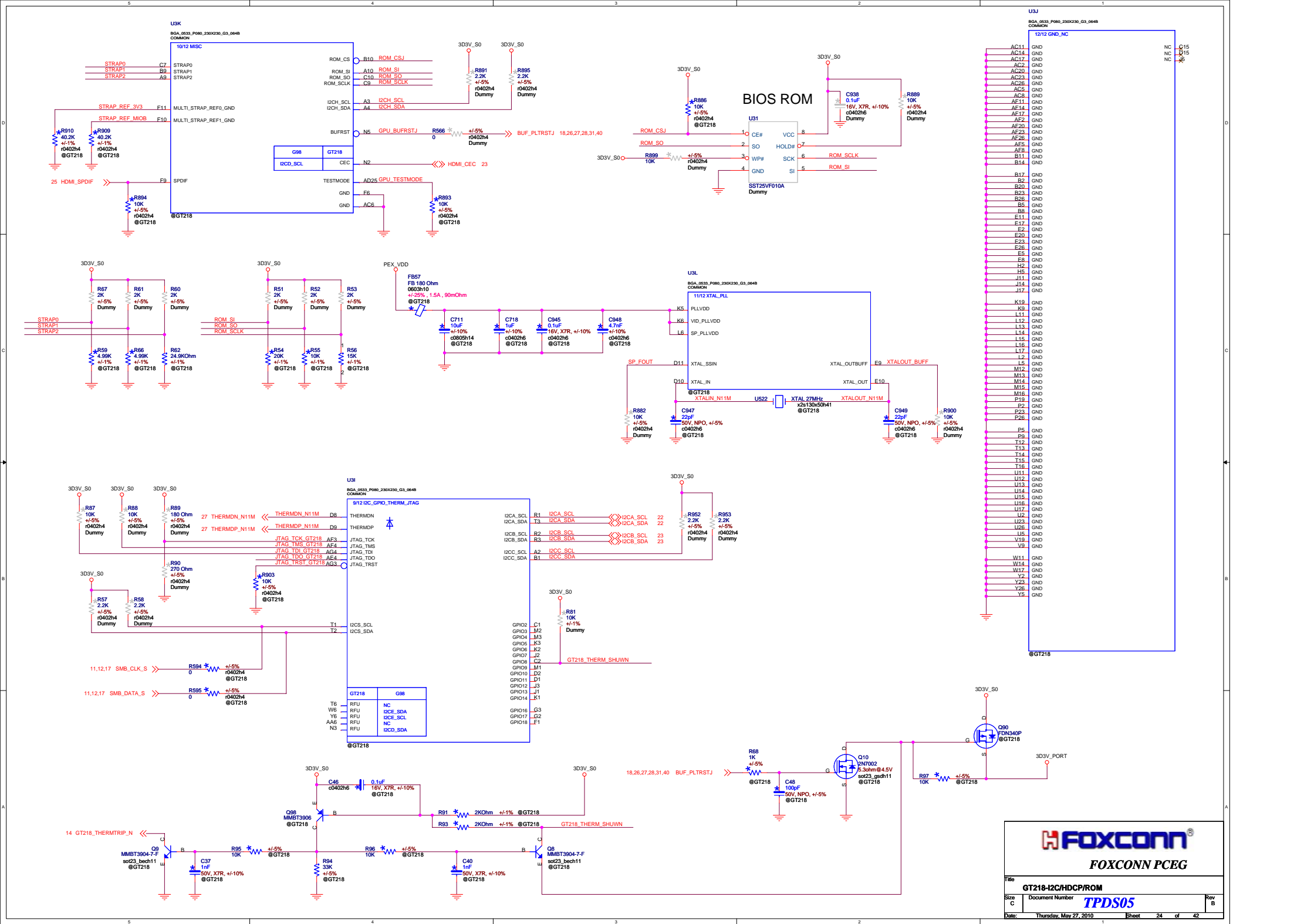
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GT218-VGA CONN			
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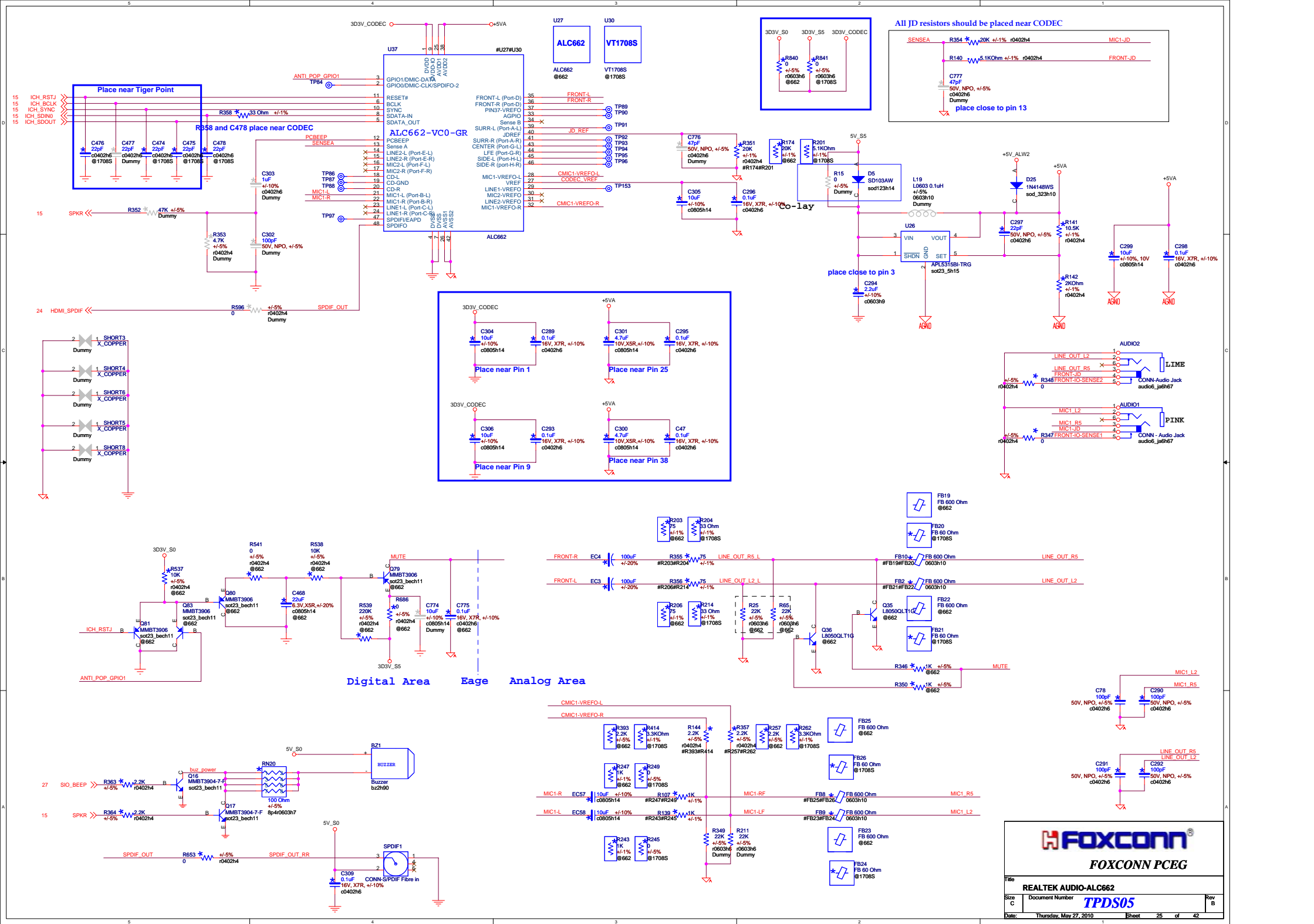
GT218-ION HDMI Interface



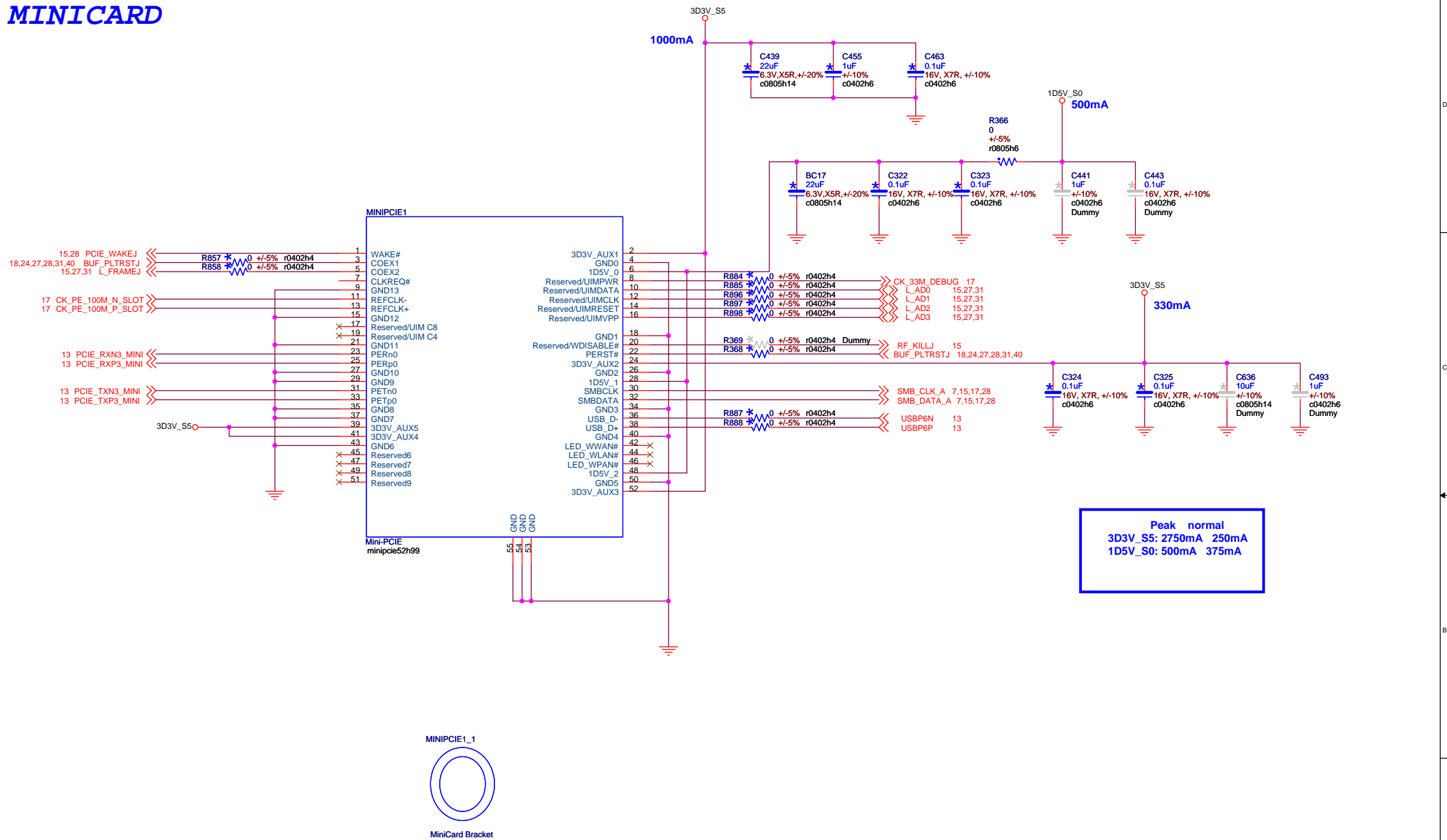
GT218-ION Unused IFP Display Interface







MINICARD



Peak normal
3D3V_S5: 2750mA 250mA
1D5V_S0: 500mA 375mA

	Symbol	value	Description
JP3 (pin 124)	Flashseg1_EN	1	Disable
		0	Flash I/F Address Segment1 is enable
JP4 (pin 126)	K8PWR_EN	1	K8 power sequence function is disable
		0	K8 power sequence function is enable
JP3 & JP5 (pin124&pin 46)	FAN_CTL_SEL	11	The default of EC index 63h/6Bh/73h is 80h
		10	The default of EC index 63h/6Bh/73h is FFh
		01	The default of EC index 63h/6Bh/73h is 00h
		00	The default of EC index 63h/6Bh/73h is 40h
JP5 (pin 46)	WDT_EN	1	Disable WDT
		0	Enable WDT to reset PWROK1/2/3

9 CPU_THERMDA >> 1 2 CPU_THERMDA_SIO

ST1
X COPPER
Dummy

9 CPU_THERMDC >> 1 2

ST2
X COPPER
Dummy

THERMDA/THERMDC

3.3nF
+/-10%

c0402h6

Q18
MMBT3904-7-F
sot23, bech11
Dummy

Near SIO

TS D-

1. width=10 mils, spacing=20mils.
2. route the lines in parallel

24 THERMDP_N11M>> 1 2 N11M_THERMDA_SIO

3.3V
±10%

Near SIO

ST3
X_COPPER
Dummy

c0402h6
@GT218

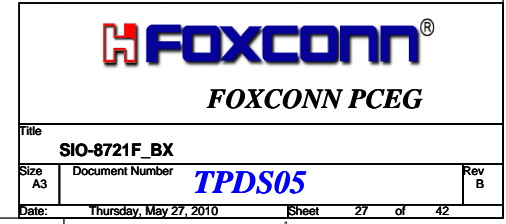
Q19
MMBT3904-7-F
sol23_bech11
Dummy

24 THERMDN_N11M>> 1 2

ST4
X_COPPER
Dummy

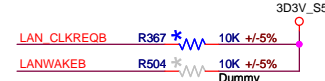
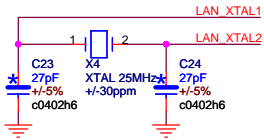
THERMDA/THERMDN
1. width=10 mils, spacing=20mils.
2. route the lines in parallel

TS D-

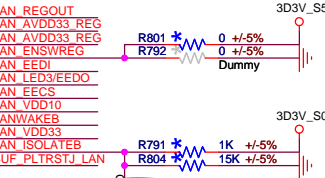
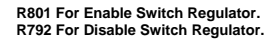
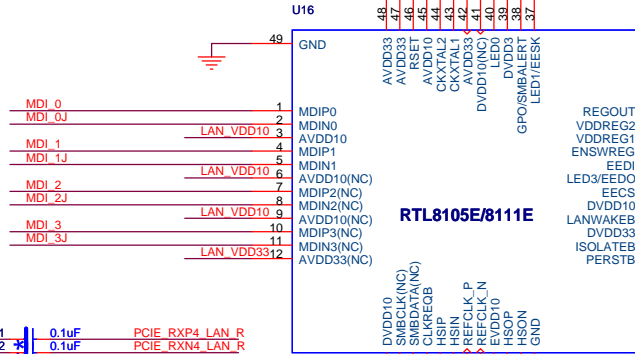
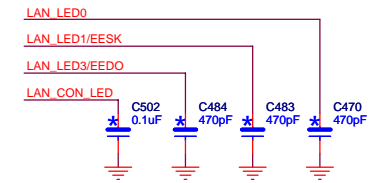


PCIE LAN Chipset

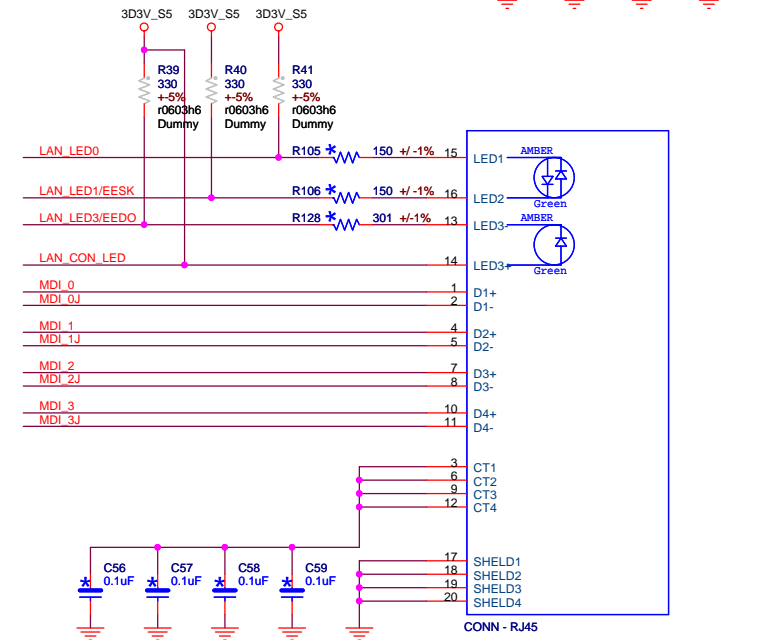
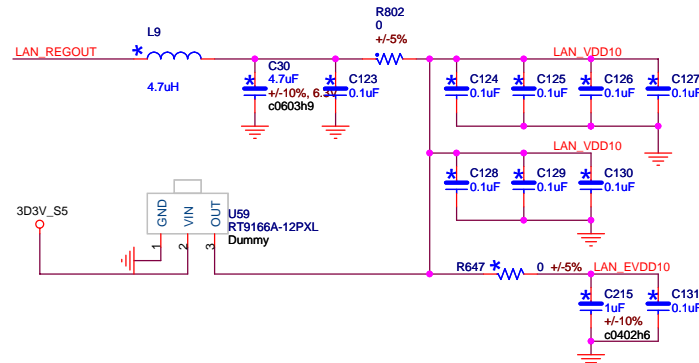
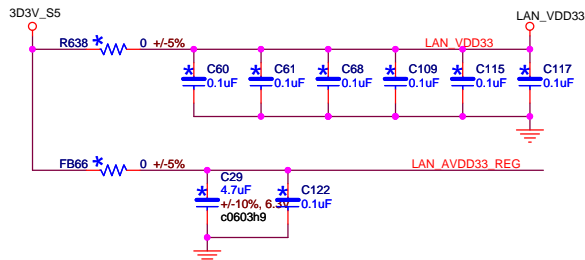
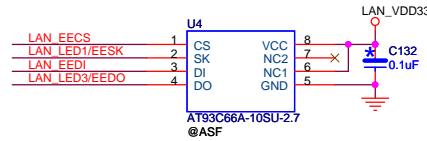
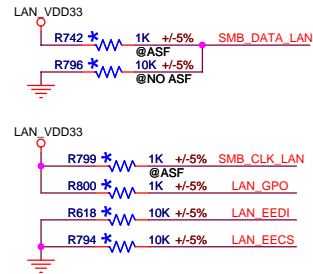
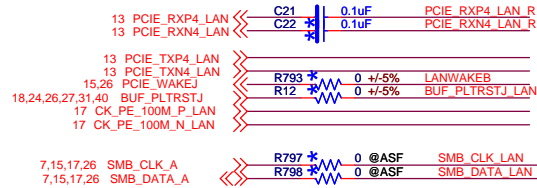
R795 value should be 2.49K (1%) for all application.



10K ohm close to Host side



Modify by Adwin 2010-01-20
When release FAB B BIOS,
pull this GPIO LOW(not disable)



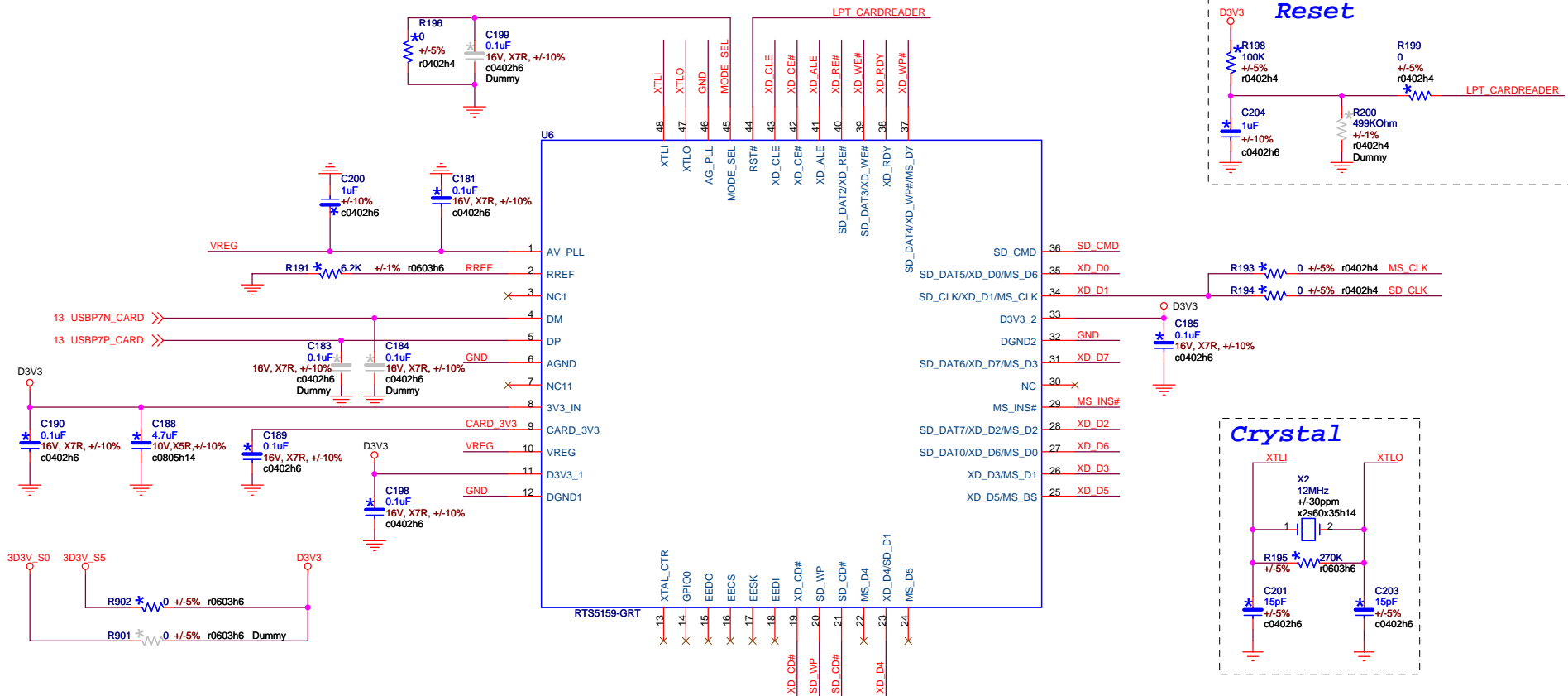
FOXCONN PCEG

Title	LAN-REALTEK_RTL8111E
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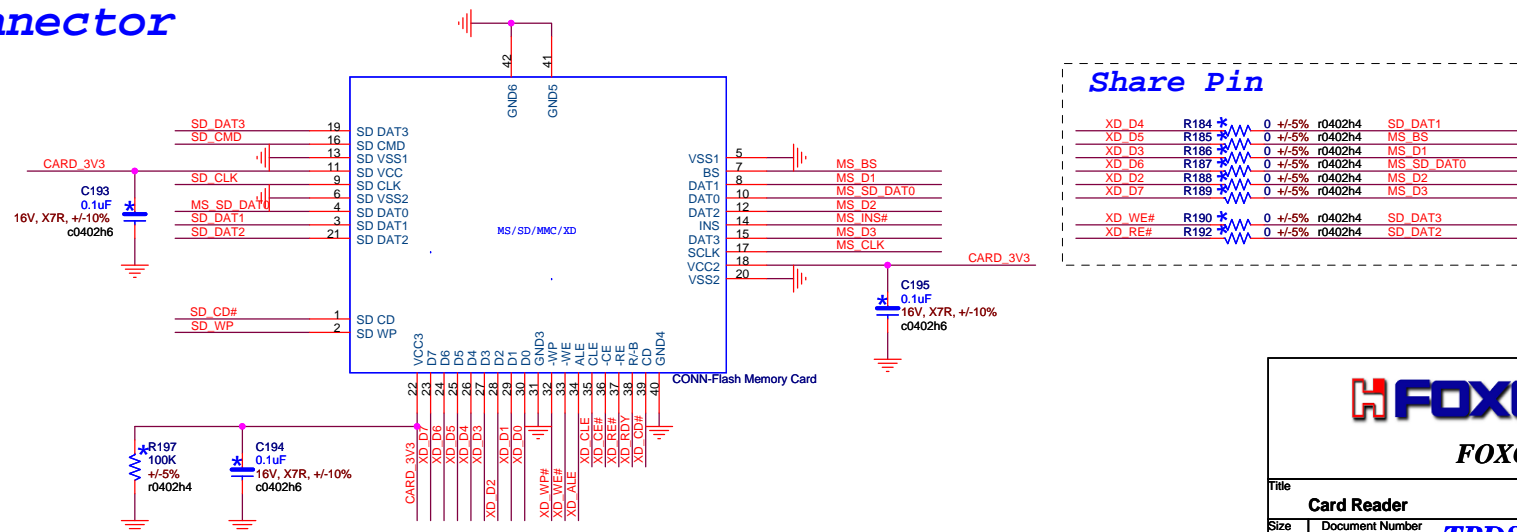
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Card Reader Chipset



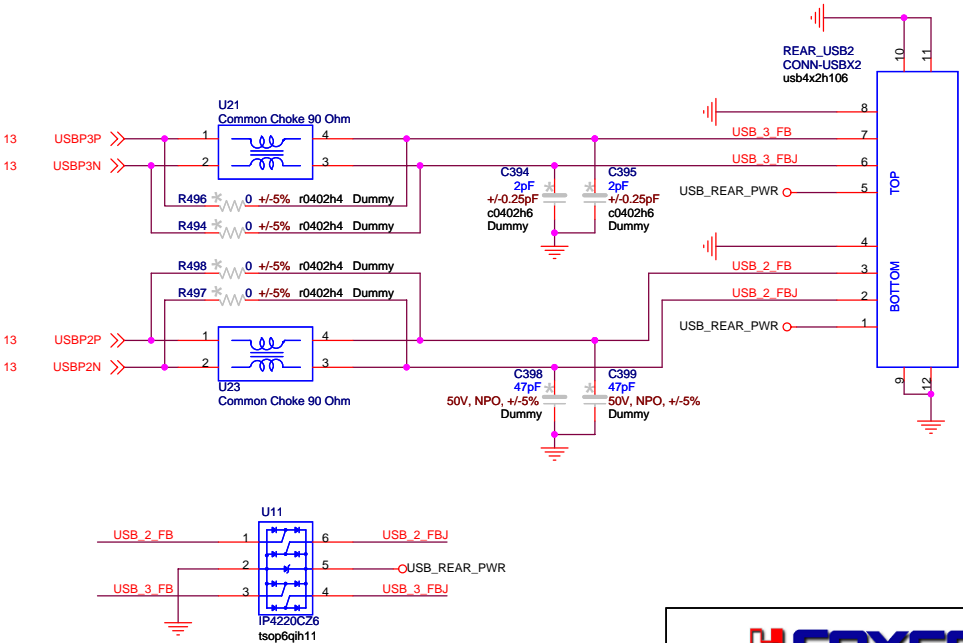
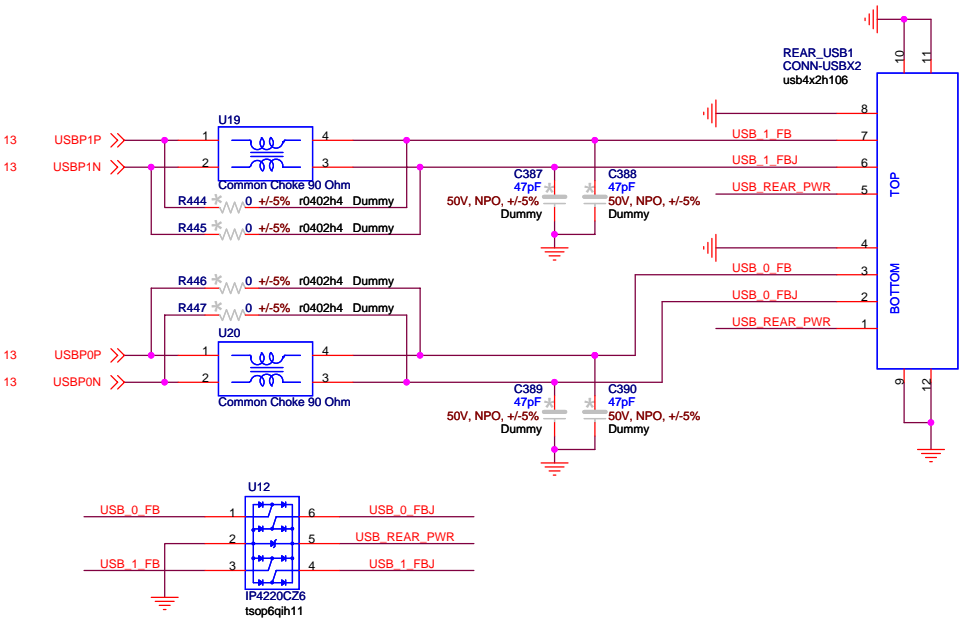
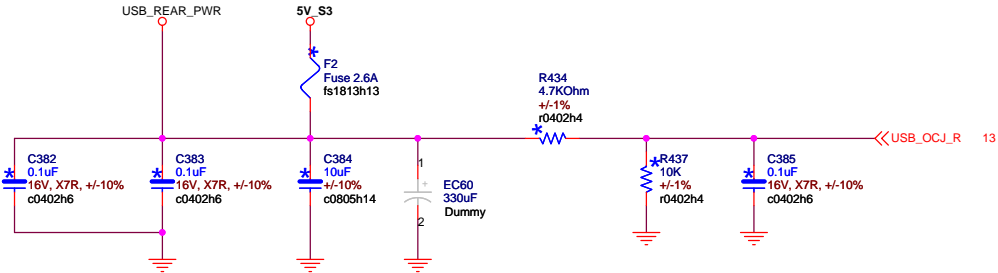
Card Reader Connector



FOXCONN®
FOXCONN PCEG

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Card Reader			
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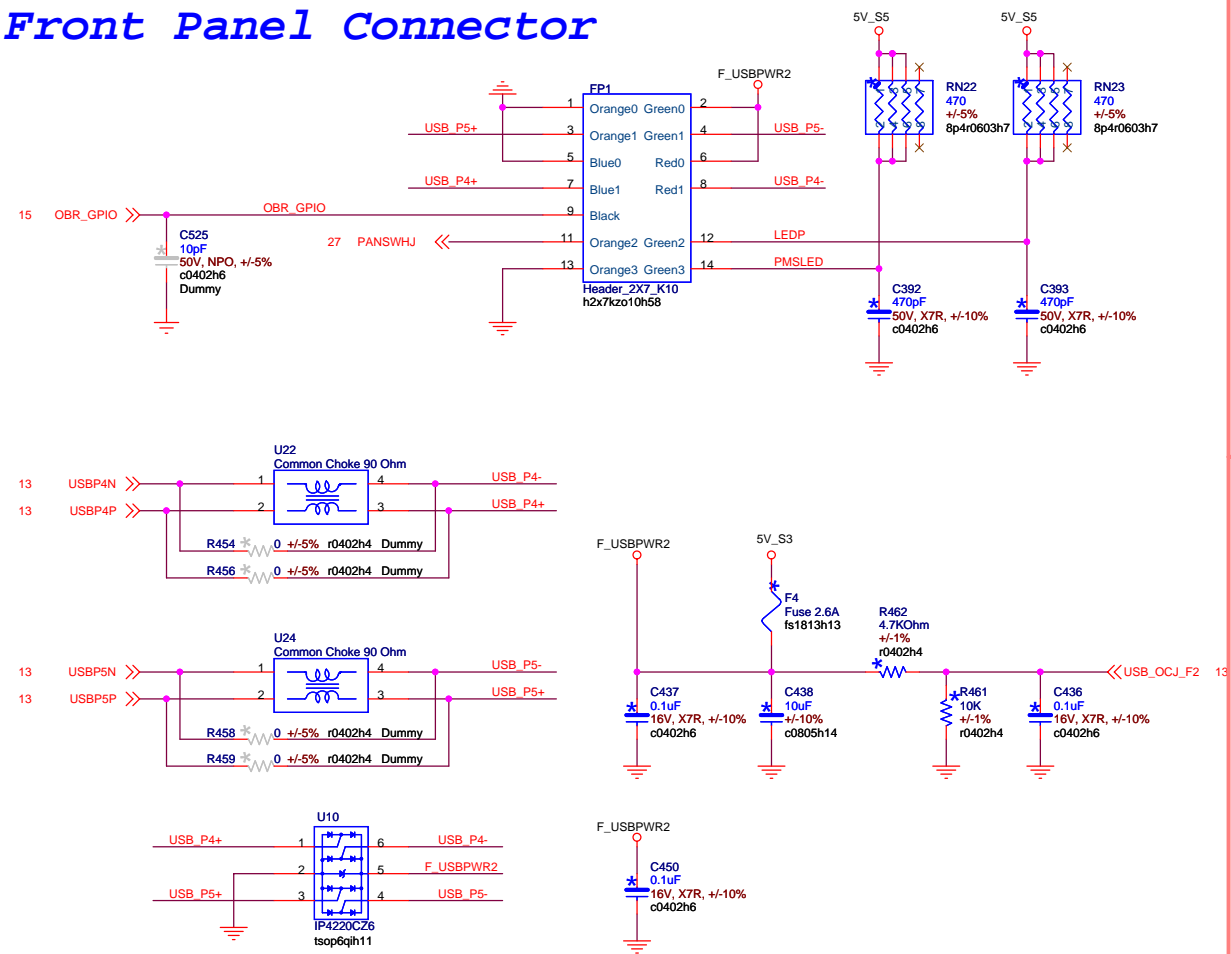
REAR USB1



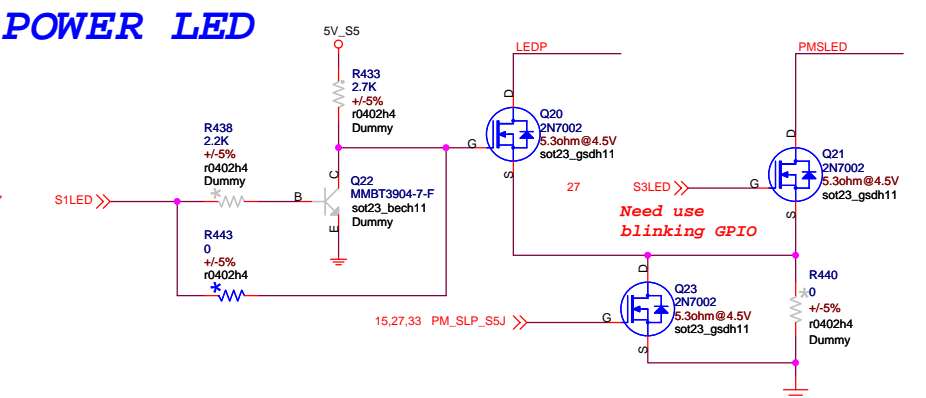
FOXCONN PCEG

Title		Rear USB	
Size	A3	Document Number	TPDS05
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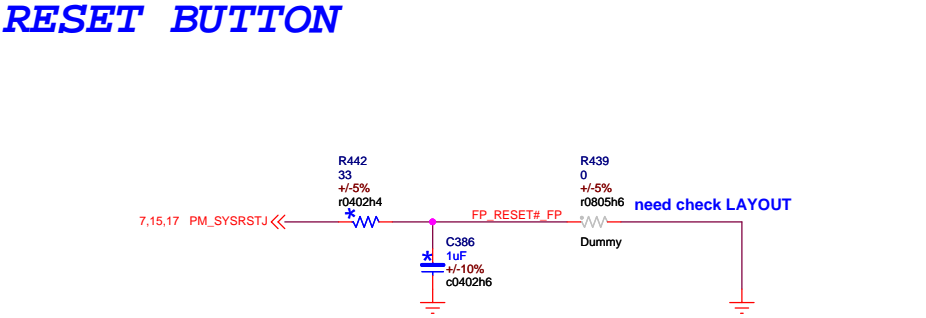
Front Panel Connector



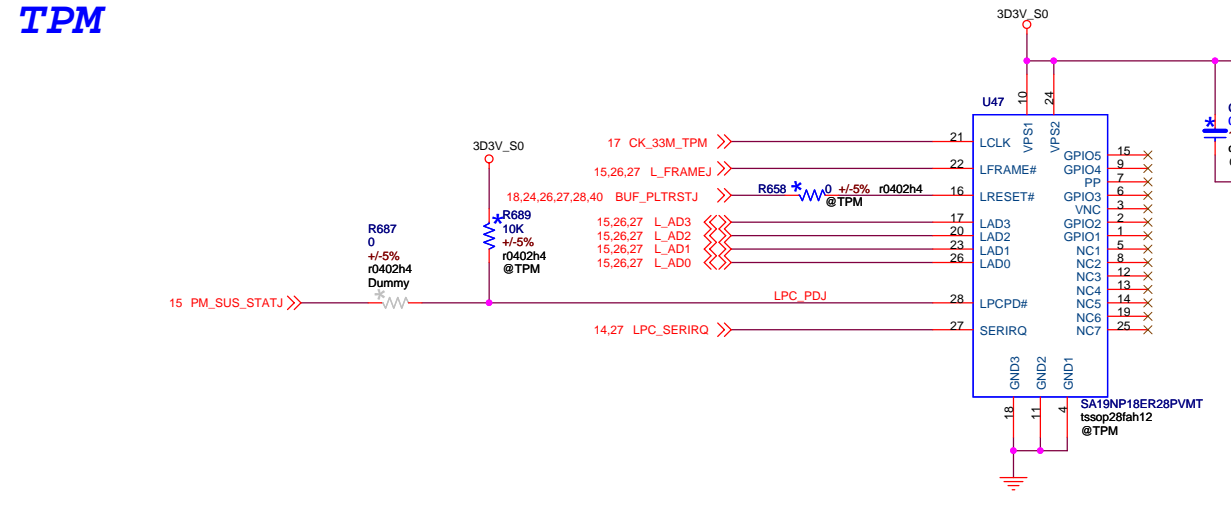
POWER LED




RESET BUTTON



TPM



Reserved for reset Button



FOXCONN PCEG

Title

Front Panel & TPM

Size

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SYS FAN

5V_S0

R460 4.7K +/-5% r0402h4

R463 100 Ohm +/-1% r0402h4

FANPWM 27

5V_S0

R465 4.7K +/-1% r0603h6

D18 1N4148WS sod_323h10

FANTAC 27

C403 100pF 50V, NPO, +/-5% c0402h6

C404 0.1uF 16V, X7R, +/-10% c0402h6

C405 10uF +/-10% 0805h14

C406 470pF 50V, X7R, +/-10% c0402h6

D19 MMSZ2V4T1 sod123h14 Dummy

SYS_FAN1 Header_1X4 hs8104h47

G2 4 3 2 1

G1 1

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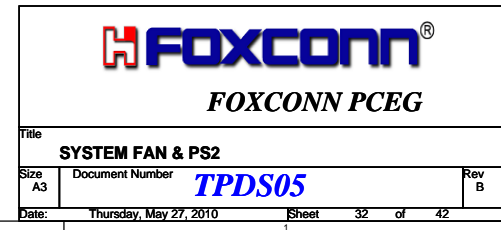
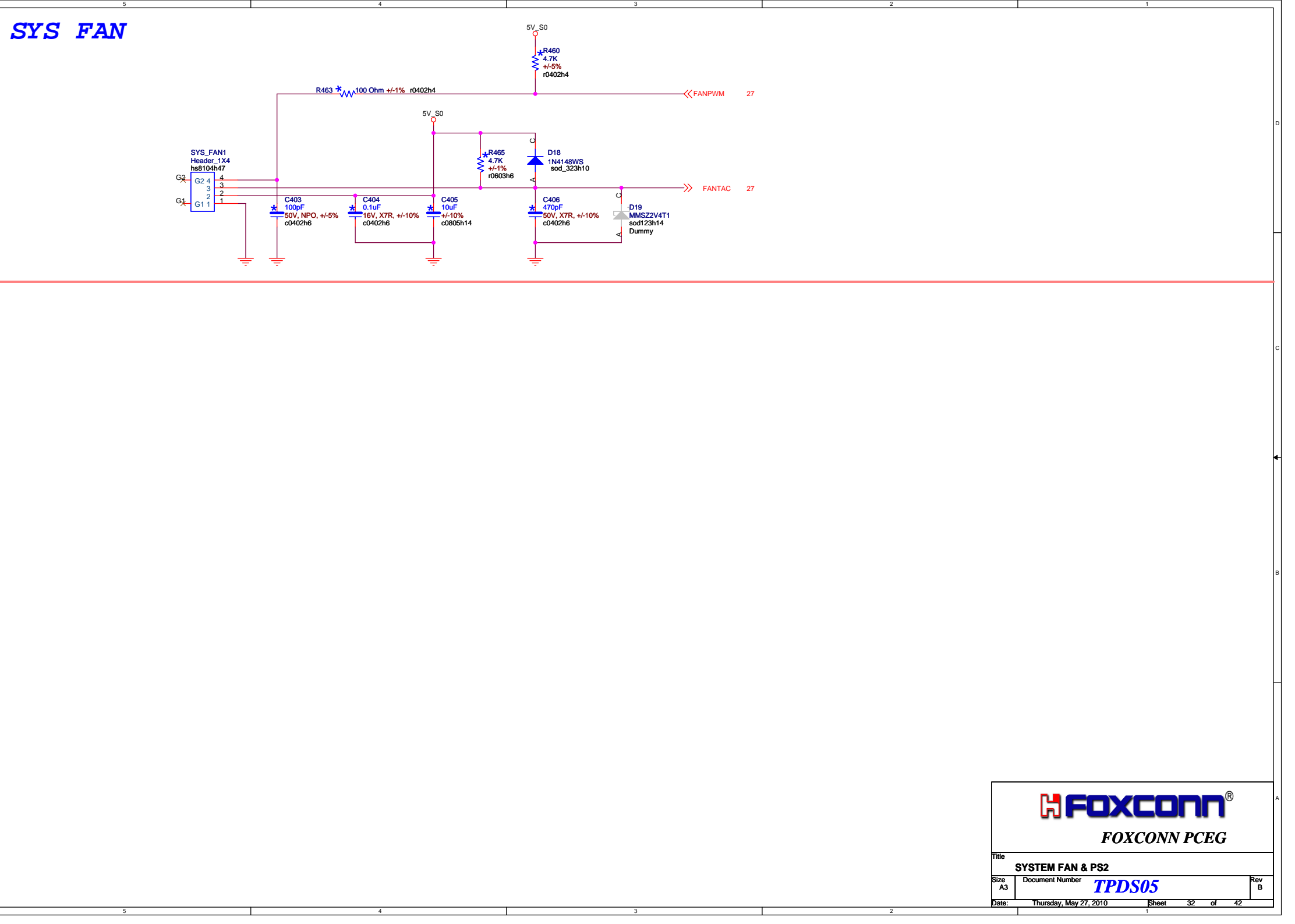
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SYSTEM FAN & PS2


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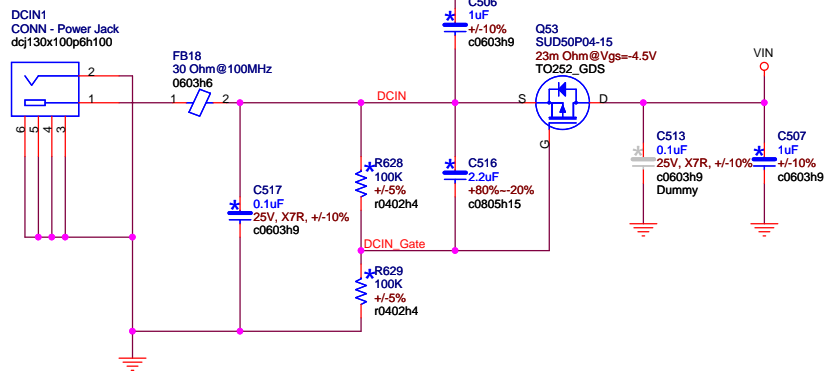
				
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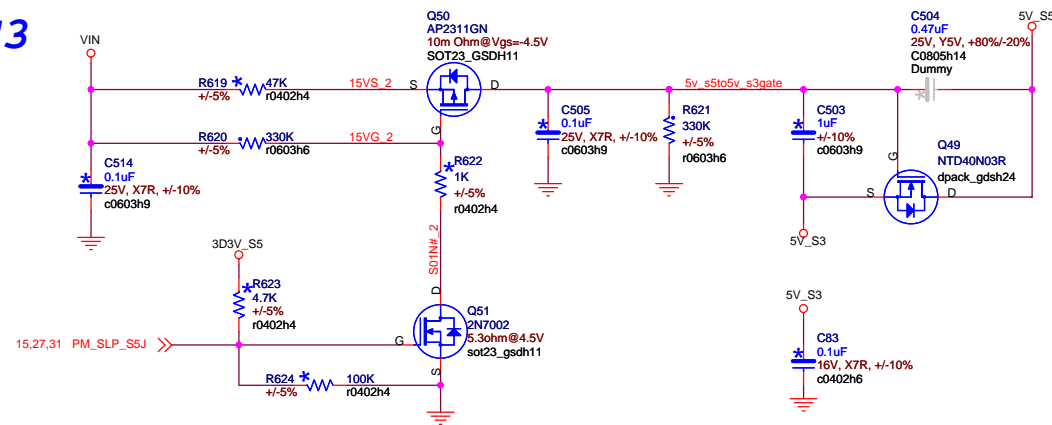
				
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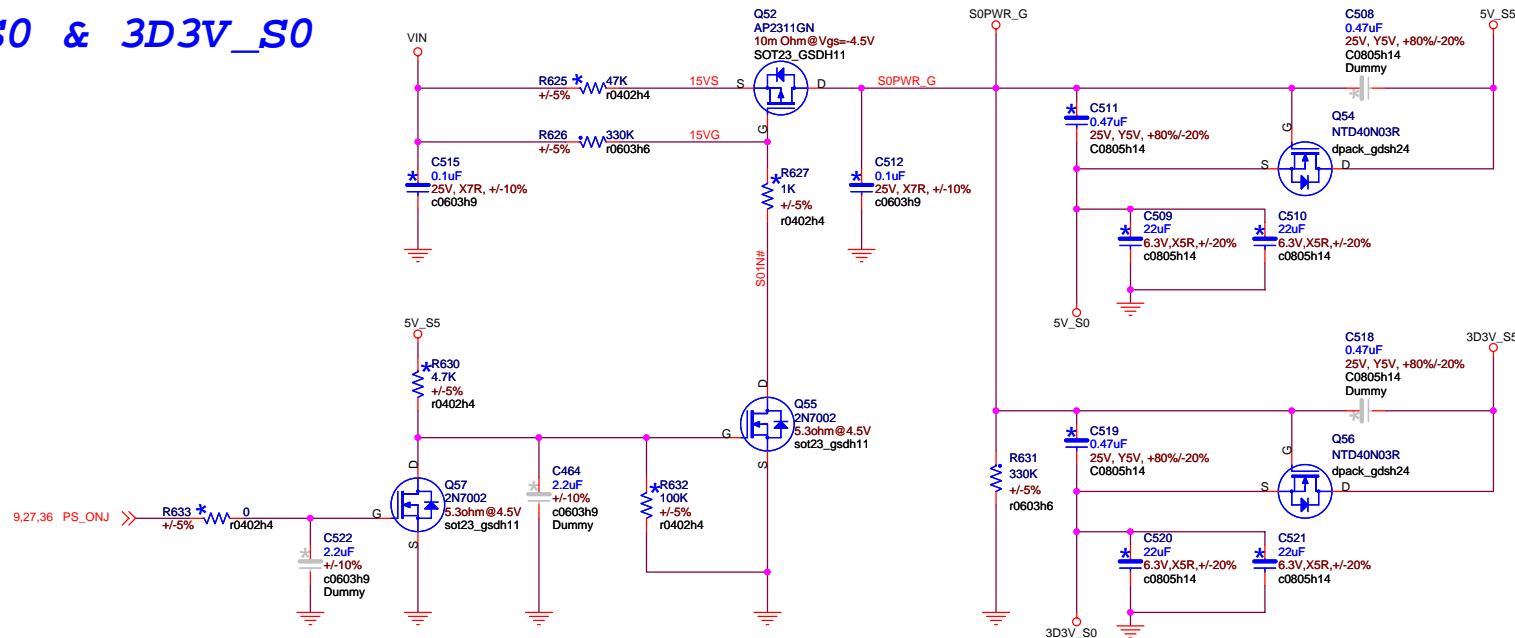
VIN



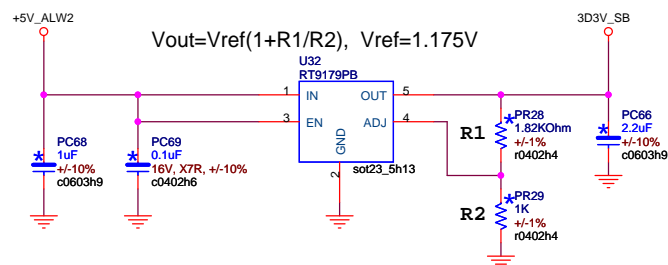
5V_S3



5V_S0 & 3D3V_S0



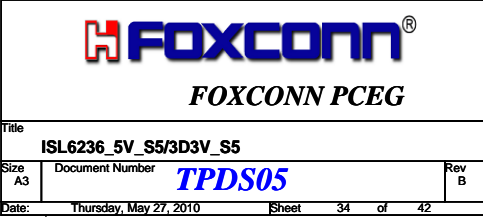
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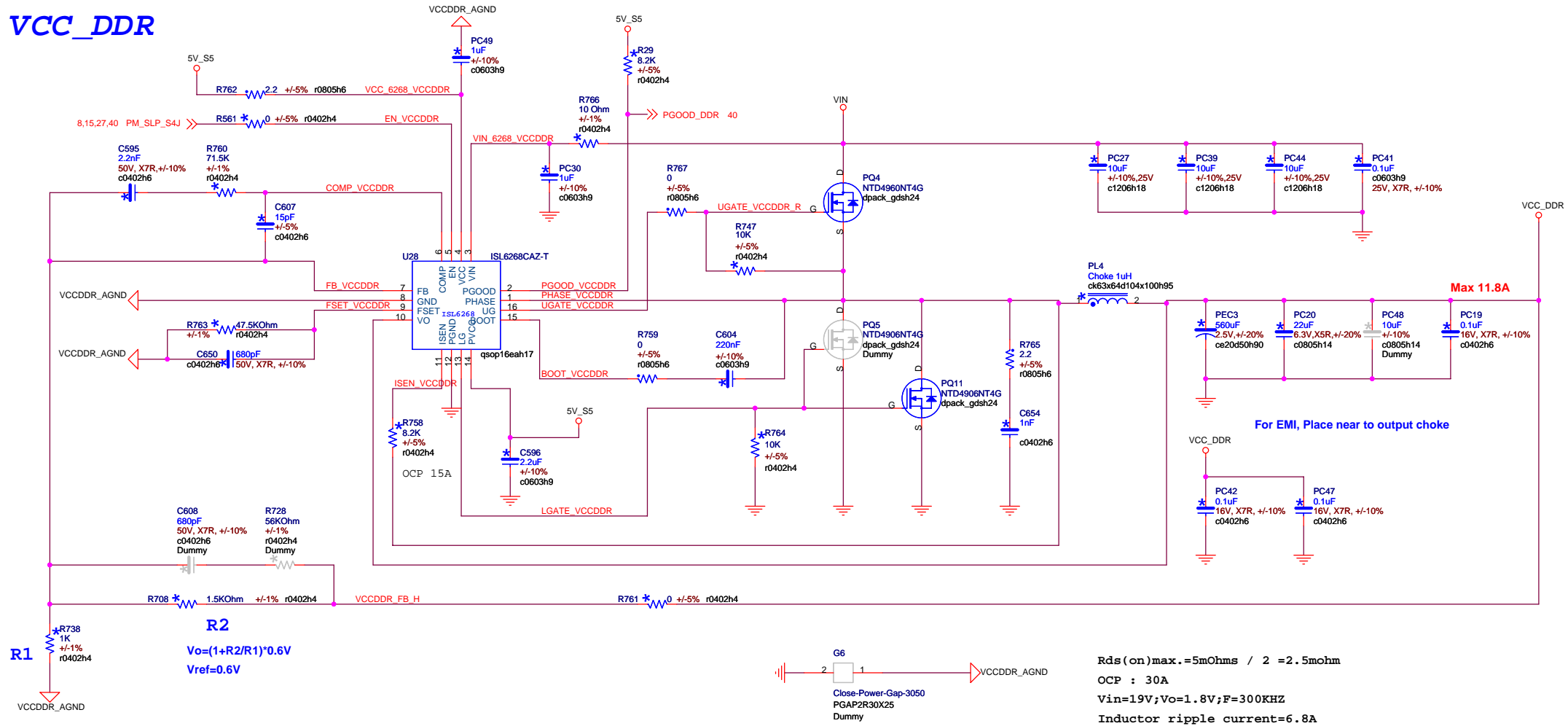
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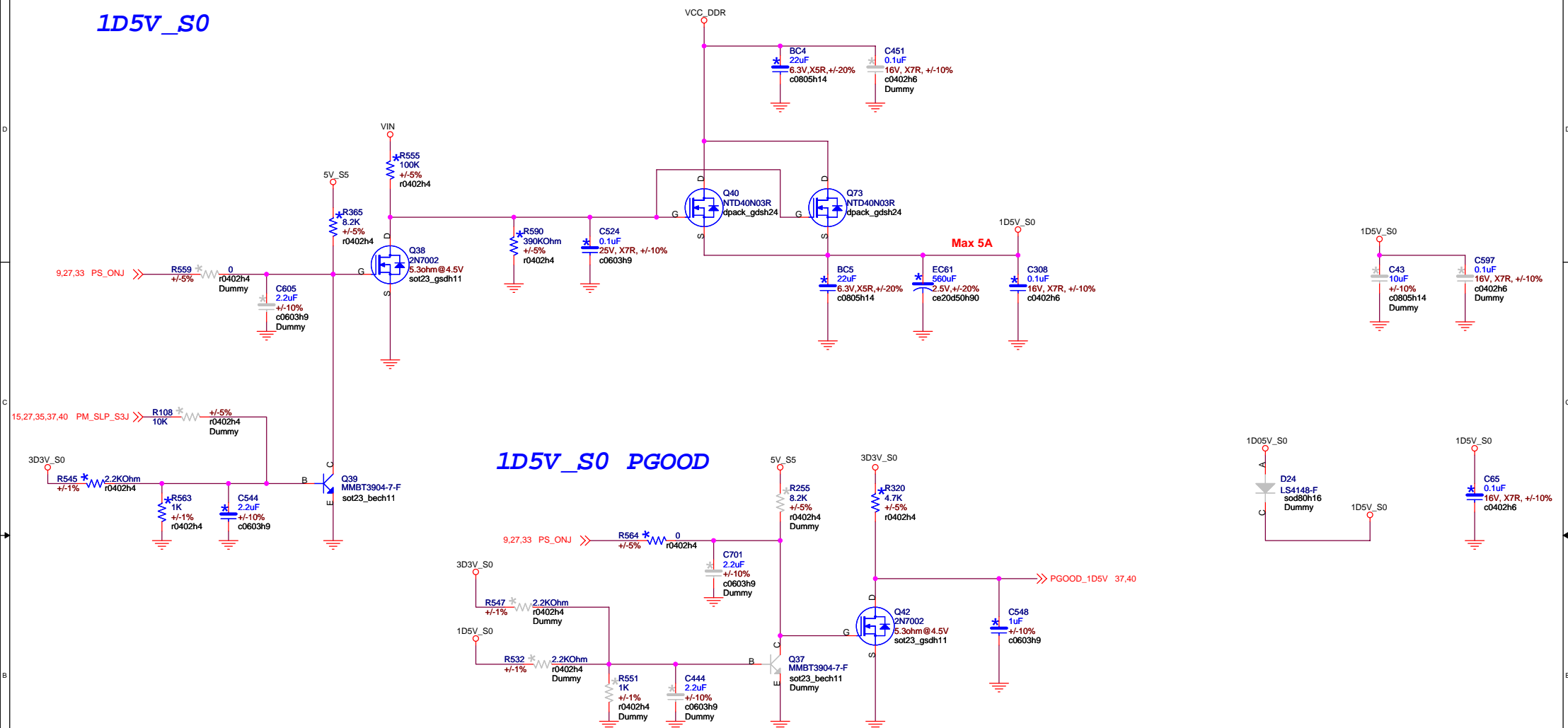
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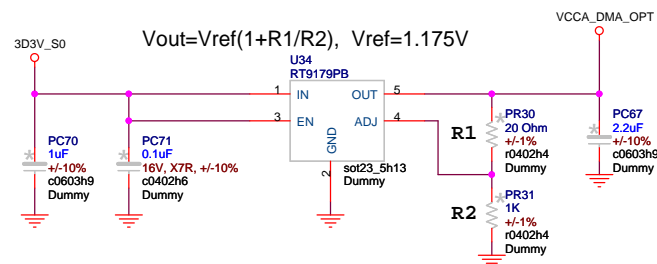
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1D5V_S0



VCCA_DMA_OPT for Pine Trail D Rev_A0



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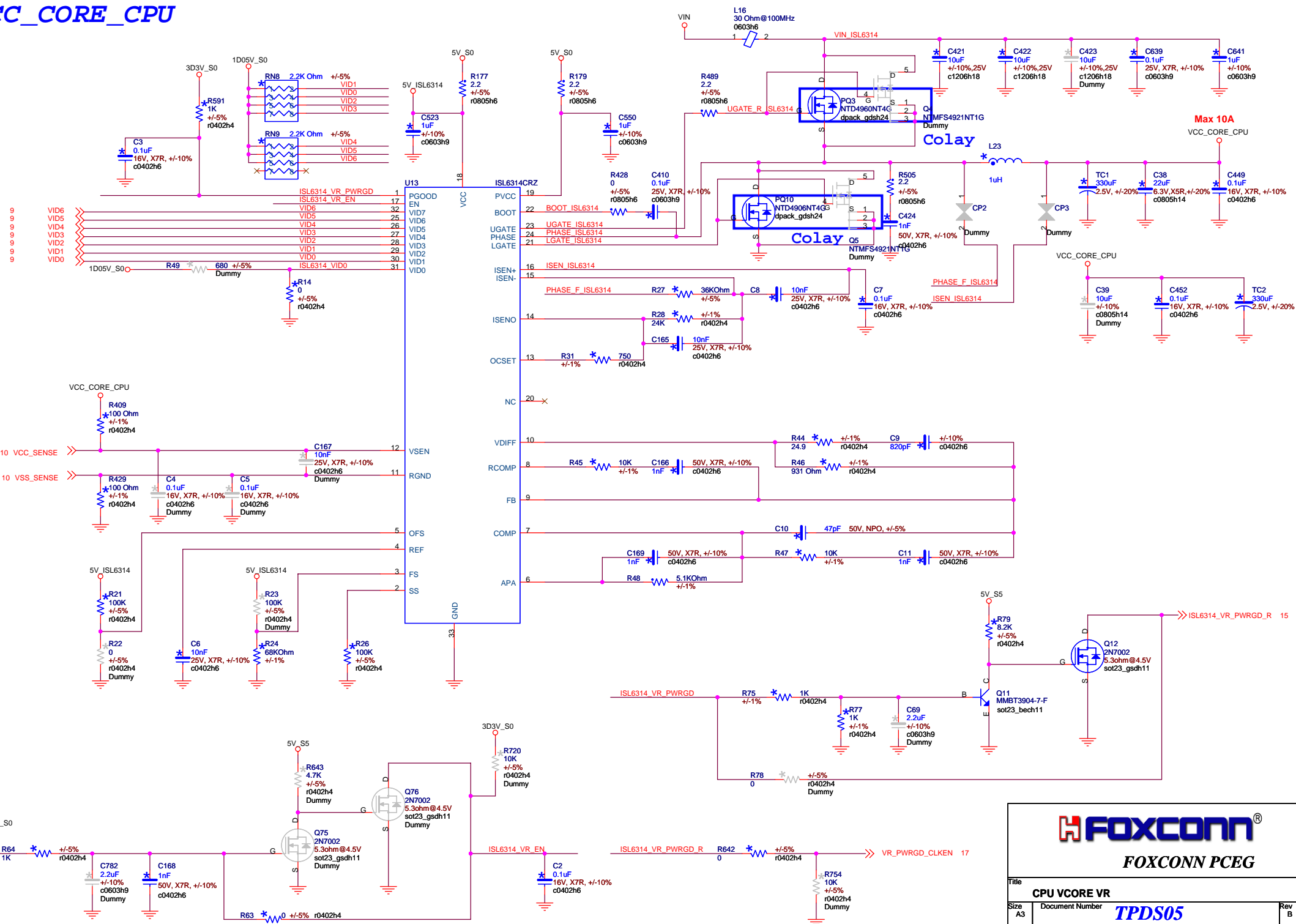
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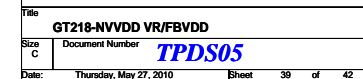
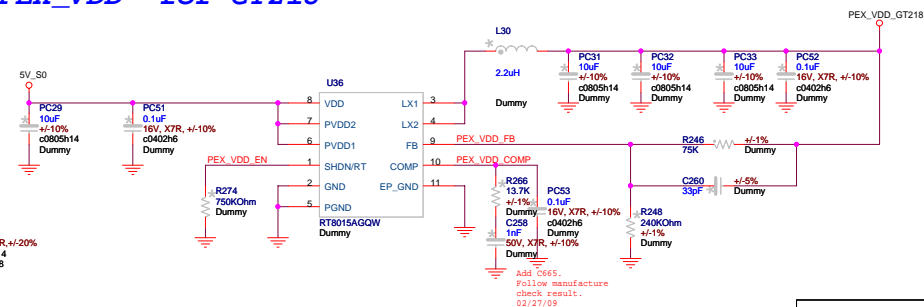
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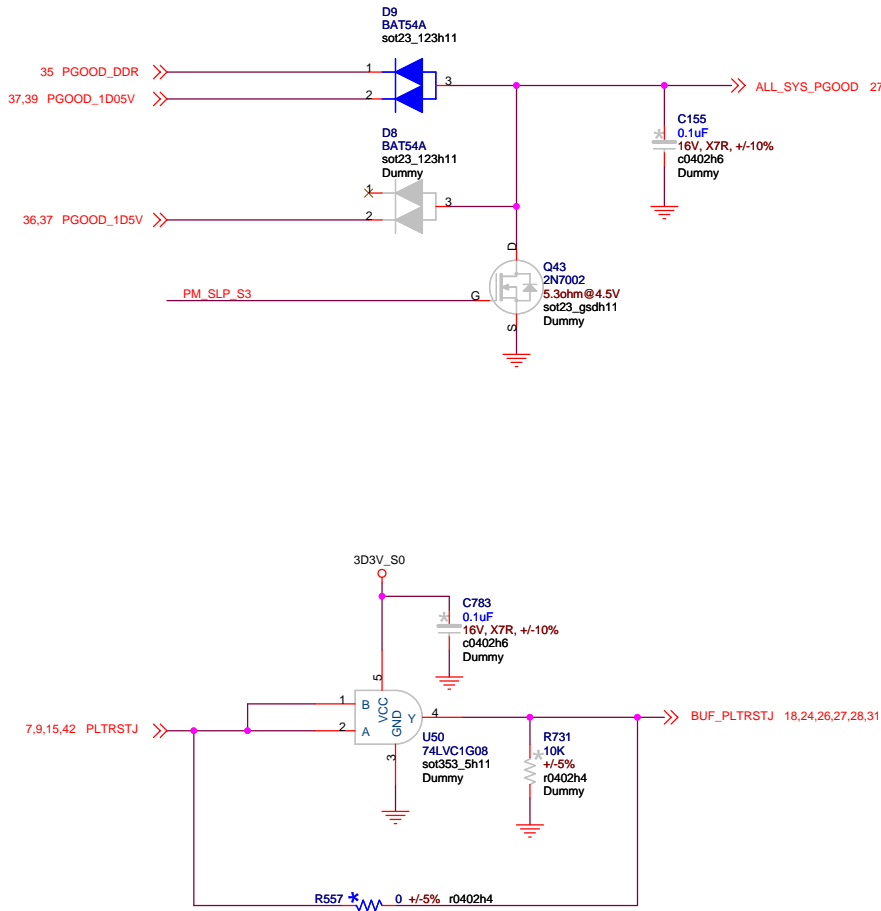
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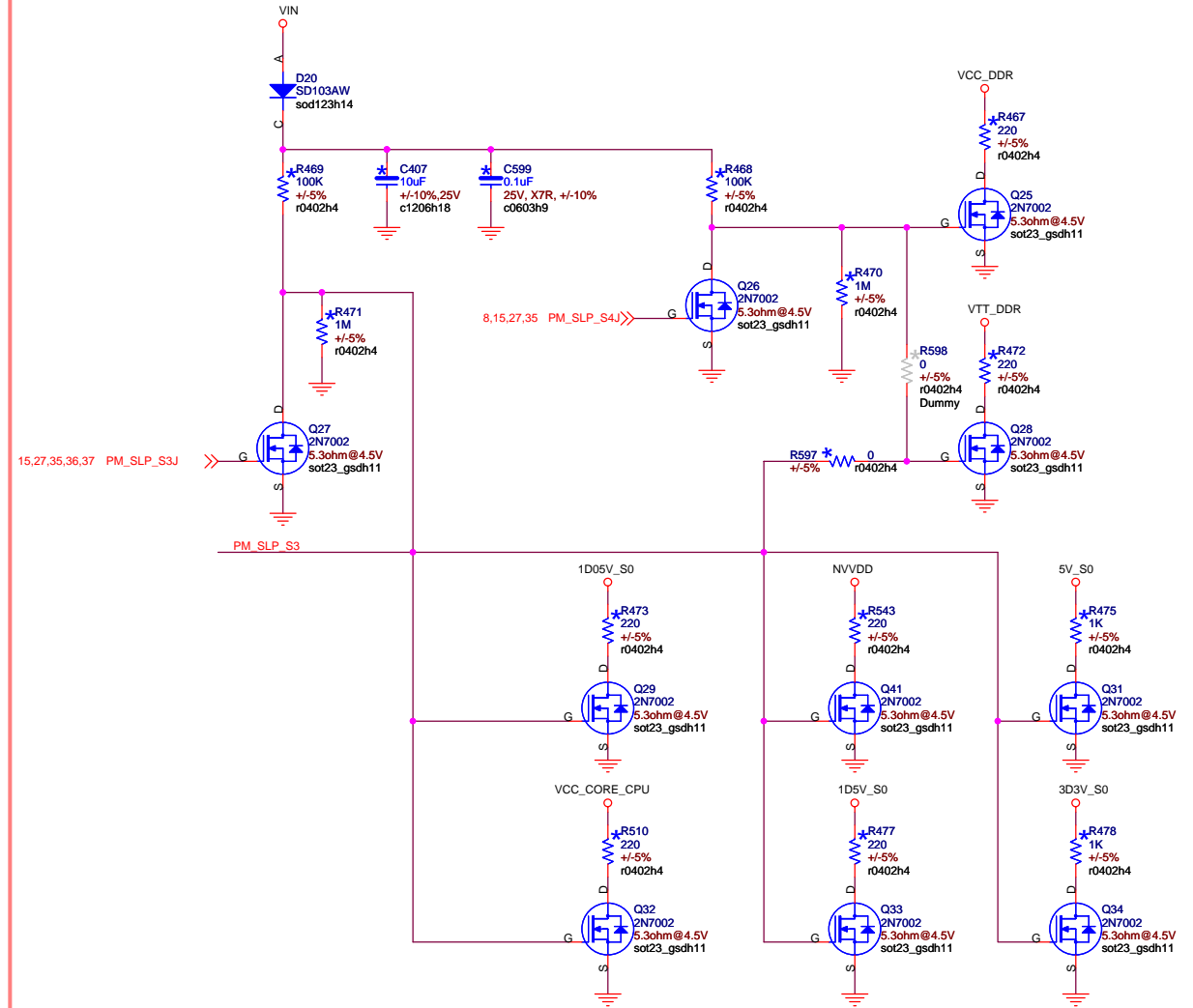
PEX_VDD for GT218



POWER GOOD LOGICS



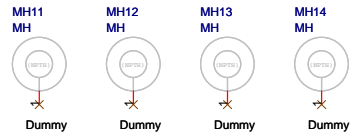
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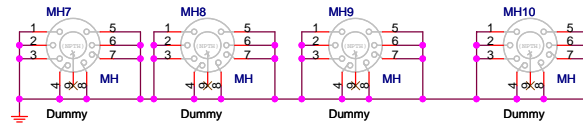
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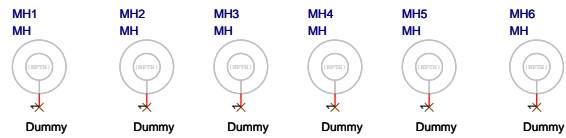
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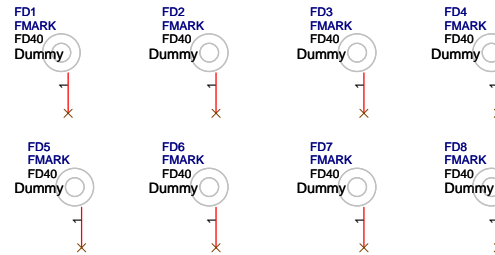
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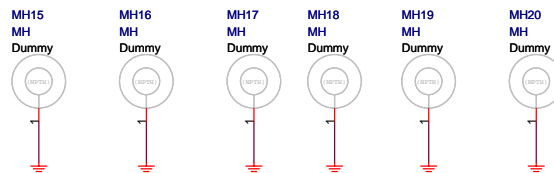
For HDD



For MASK

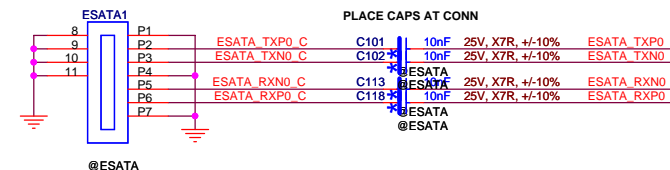
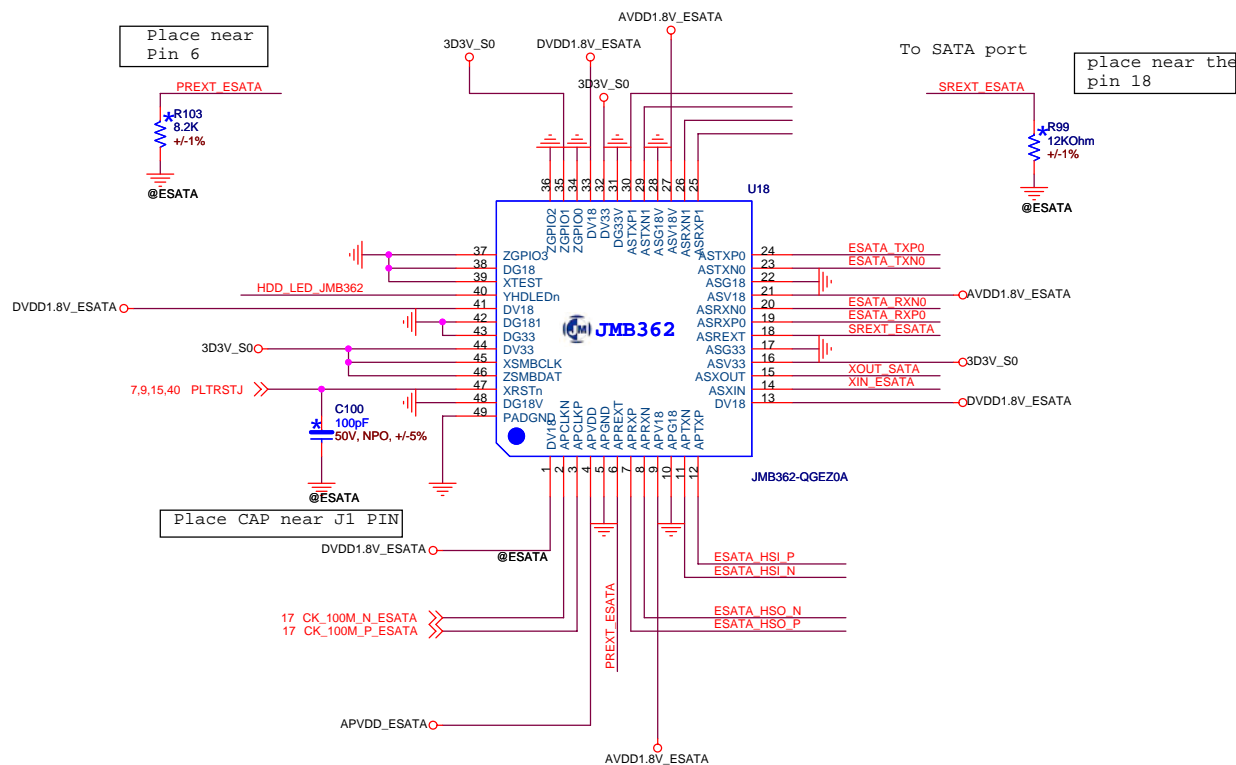
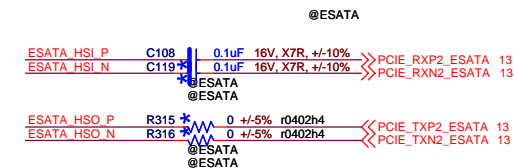
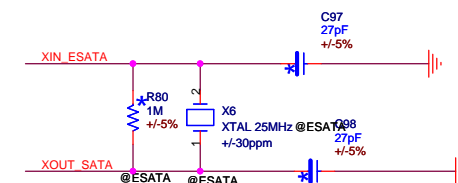
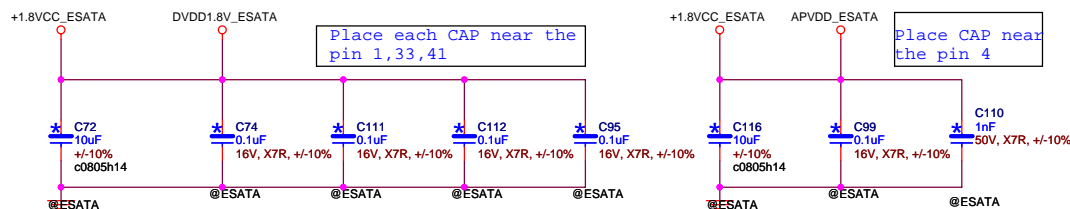
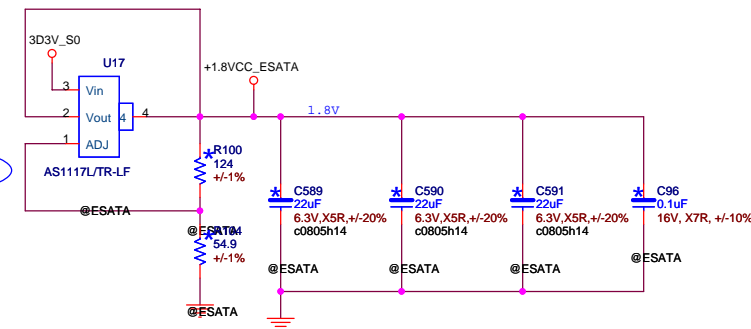
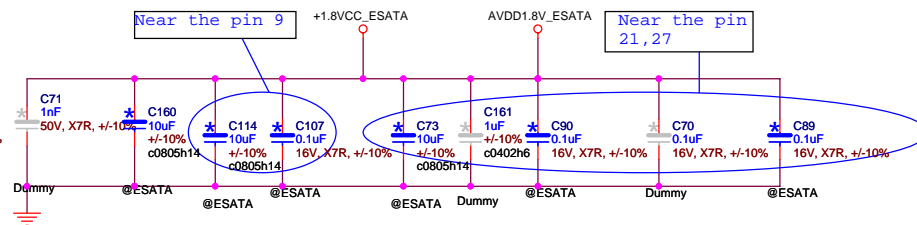
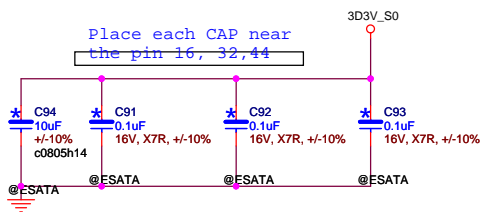


For EMC



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